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the h 265 4k video encoder ip core is a new high performance engine from soc it uses soc s all hardware architecture and efficient design methodology to offer superior performance deliverables ip core as an fpga targeted netlist core instantiation reference design integration guide api register guide specifications product evaluation kits the mpeg transport encoder multiplexer is usually included in an encoder ip core soc also provides network modules tcp udp ip ethernet mac ip cores which can be licensed along with the h 265 video audio encoder ip cores customized versions of these products are available on request the soc h 264 avc video encoder ip core is a single chip solution that supports single or multi stream h 264 video encoding for all industrial standard resolutions including qvga sd and hd up to 1080p 120fps the h 264 4k encoder and 8k encoder ip cores are released as separated products h 264 video encoder ip core h 264 h 264 avc state key lab of asic system fudan university 复旦大学专用集成电路与 video image processor vip lab vip lab h 264 feature avc h 264 baseline profile yuv 4 2 0 bitdepth 8 fhd 60fps gop i p h 264 15 encoder ip core the new h 264 15 core represents a huge leap in performance with just a small increase in size over our previous generation core encoding and decoding performance have been doubled with just a 30 increase in gate count the ip core provides high performance encode and decode capability up to 4k60fps with a single core architecture and an optimized silicon area for 4k ultra hd applications such as security ip cameras wearable cameras drone cameras automotive nvr and more h 265 havc video audio encoder ip core for xilinx fpgas zero latency and high video quality the h 264 enc1 encoder ip is a high performance hd compression core ip that implements the h 264 mpeg 4 avc video coding standard this encoder core compresses hd and sd video through the use of advanced algorithms and can be implemented in a low cost fpga the h264 hp e core from alma technologies is an advanced and self contained itu t h 264 high profiles hardware encoder it supports real time encoding of 4 2 0 and 4 2 2 video streams in 8 10 or 12 bit per component color depth allegro dvt proposes these multiformat encoder and decoder ips ensuring selectable video codecs scalability through a multicore architecture and ultra low latency all on a minimum silicon area the h 264 avc hd video encoder ip core is a real time encoder of hd video or video audio data a single core can encode video of up to 1920 1080 resolution at up to 120fps the latency is 0 25ms and 0 125ms for 30fps and 60fps respectively close to zero latency between input and output our h 264 encoders are designed to achieve high throughput with low clock frequencies the cores can fit and process full hd video in low cost fpga device families for example full hd 1080p30 video can be encoded in real time using intel cyclone iii iv v or xilinx artix 7 devices the soc h 264 avc 8k video encoder is a single chip solution that supports single or multi stream h 264 avc video encoding for 8k 7680x4320 resolution at the frame rate of 30fps or 60fps the soc 16k encoder ip core is released as separated product bch ip core encoder the bch encoder has a parallel architecture with an input and output of d data bits when the encoder receives data symbols it generates check symbols for a given codeword and sends the input codeword with the check symbols to the output interface this repository contains approximately 860 free and open source vhdl verilog ip cores all these cores have been carefully scraped from opencores org using a quite long python script available here our video encoder ip core is a real time true multi format hardware encoder ip supporting h 264 avc h 265 hevc vp9 video codecs and jpeg still image standard the encoder ip escort carry wave663 is a 4k 8k multi standard encoder video ip that supports hevc h 265 and avc h 264 video codec standards it provides 4k120fps 500mhz or 8k60fps 1ghz real time encoding performance with a dual core solution wave663 is based on wave6 architecture which is the latest c m video architecture this jpeg compression ip core supports the baseline sequential dct modes of the iso iec 10918 1 standard it implements an area efficient high performance hardware jpeg encoder with remarkably low processing latency the soc h 264 avc video encoder ip core is a single chip solution that supports single or multi stream h 264 video encoding for all industrial standard resolutions including qvga sd and hd up to 1080p 120fps the soc h 264 video encoder ip core supports fpgas of xilinx altera and microsemi the bose chaudhuri and hocquenghem bch error correction intellectual property ip core is typically used in nand flash applications the bch intel fpga ip core is often used as a companion code with other forward error correction fec ip cores such as the reed solomon and low density parity check ldpc ip cores

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the mpeg transport encoder multiplexer is usually included in an encoder ip core soc also provides network modules tcp udp ip ethernet mac ip cores which can be licensed along with the h 265 video audio encoder ip cores customized versions of these products are available on request

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the soc h 264 avc video encoder ip core is a single chip solution that supports single or multi stream h 264 video encoding for all industrial standard resolutions including qvga sd and hd up to 1080p 120fps the h 264 4k encoder and 8k encoder ip cores are released as separated products

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h 264 15 encoder ip core the new h 264 15 core represents a huge leap in performance with just a small increase in size over our previous generation core encoding and decoding performance have been doubled with just a 30 increase in gate count

4k video codec ips chips media inc

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the ip core provides high performance encode and decode capability up to 4k60fps with a single core architecture and an optimized silicon area for 4k ultra hd applications such as security ip cameras wearable cameras drone cameras automotive nvr and more

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the h 264 enc1 encoder ip is a high performance hd compression core ip that implements the h 264 mpeg 4 avc video coding standard this encoder core compresses hd and sd video through the use of advanced algorithms and can be implemented in a low cost fpga

high profiles h 264 encoder ip core alma technologies

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the h264 hp e core from alma technologies is an advanced and self contained itu t h 264 high profiles hardware encoder it supports real time encoding of 4 2 0 and 4 2 2 video streams in 8 10 or 12 bit per component color depth

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allegro dvt proposes these multiformat encoder and decoder ips ensuring selectable video codecs scalability through a multicore architecture and ultra low latency all on a minimum silicon area

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our h 264 encoders are designed to achieve high throughput with low clock frequencies the cores can fit and process full hd video in low cost fpga device families for example full hd 1080p30 video can be encoded in real time using intel cyclone iii iv v or xilinx artix 7 devices

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3 1 bch ip core encoder intel

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bch ip core encoder the bch encoder has a parallel architecture with an input and output of d data bits when the encoder receives data symbols it generates check symbols for a given codeword and sends the input codeword with the check symbols to the output interface

github fabriziotappero ip cores a huge collection of vhdl

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this repository contains approximately 860 free and open source vhdl verilog ip cores all these cores have been carefully scraped from opencores org using a quite long python script available here

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our video encoder ip core is a real time true multi format hardware encoder ip supporting h 264 avc h 265 hevc vp9 video codecs and jpeg still image standard the encoder ip escort carry

8k video encoder ips chips media inc

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wave663 is a 4k 8k multi standard encoder video ip that supports hevc h 265 and avc h 264 video codec standards it provides 4k120fps 500mhz or 8k60fps 1ghz real time encoding performance with a dual core solution wave663 is based on wave6 architecture which is the latest c m video architecture

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