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what is axi axi which means advanced extensible interface is an interface protocol defined by arm as part of the amba advanced microcontroller bus architecture standard the axi3 axi4 specification are freely available on the arm website so i encourage anybody who is interested to download it there are 3 types of axi4 interfaces amba 4 0

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to recap here are the 3 main protocols included within the ace and axi specification axi advanced extensible interface axi provides connectivity for non coherent masters and slaves ace axi coherency extensions supports full coherency for masters with caches such as cortex a processors

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