Free pdf Programming and customizing the multicore propeller (Download Only)

Programming and Customizing the Multicore Propeller Microcontroller Programming and Customizing the Multicore Propeller Microcontroller: The Official Guide Multi-Core Embedded Systems Electronic Circuits for the Evil Genius 2/E tinyAVR Microcontroller Projects for the Evil Genius The TAB Battery Book: An In-Depth Guide to Construction, Design, and Use 15 Dangerously Mad Projects for the Evil Genius 30 Arduino Projects for the Evil Genius Recycling Projects for the Evil Genius Arduino + Android Projects for the Evil Genius: Control Arduino with Your Smartphone or Tablet PICAXE Microcontroller Projects for the Evil Genius Scalable Multi-core Architectures Advanced Microsystems for Automotive Applications 2016 Programming Multicore and Many-core Computing Systems VLSI 2010 Annual Symposium Hardware/Software Co-design for Heterogeneous Multi-core Platforms Software Development for Embedded Multi-core Systems Large Scale Network-Centric Distributed Systems Design of Cost-Efficient Interconnect Processing Units Modeling and Optimization of Parallel and Distributed Embedded Systems Computer Architecture Advances in Computers Euro-Par 2013: Parallel Processing Pro Netbeans IDE 6 Rich Client Platform Edition Networks-on-Chip Euro-Par 2014: Parallel Processing Workshops Embedded Android Process Algebra for Parallel and Distributed Processing Embedded Computing for High Performance The Dark Side of Silicon Big Book of Windows Hacks Next-Generation Business Intelligence Software with Silverlight 3 Reconfigurable Computing: Architectures, Tools and Applications Architecture of Computing Systems Euro-Par 2008 Workshops - Parallel Processing Introducing .NET 4.0 Euro-Par 2014: Parallel Processing The Art of Multiprocessor Programming System Design for Telecommunication Gateways Embedded Computer Systems: Architectures, Modeling, and Simulation

Programming and Customizing the Multicore Propeller Microcontroller *2010*

THE ONLY OFFICIAL GUIDE TO THE PARALLAX MULTICORE PROPELLER MICROCONTROLLER WRITTEN BY A TEAM OF PROPELLER EXPERTS THIS AUTHORITATIVE GUIDE SHOWS YOU HOW TO REALIZE YOUR DESIGN CONCEPTS BY TAKING FULL ADVANTAGE OF THE MULTICORE PROPELLER MICROCONTROLLER S UNIQUE ARCHITECTURE THE BOOK BEGINS WITH A REVIEW OF THE PROPELLER HARDWARE SOFTWARE AND SPIN LANGUAGE SO YOU CAN GET STARTED RIGHT AWAY PROGRAMMING AND CUSTOMIZING THE MULTICORE PROPELLER MICROCONTROLLER THE OFFICIAL GUIDE IS FILLED WITH A WIDE VARIETY OF STEP BY STEP HANDS ON PROJECTS PUT YOUR IDEAS INTO PRODUCTION WHEN YOU LEARN HOW TO DEBUG CODE FOR MULTIPLE CORES UNDERSTAND HOW THE PROPELLER INTERACTS WITH DIFFERENT SENSORS WIRELESSLY NETWORK PROPELLER CHIPS BUILD A BALANCING ROBOT AND CONTROL IT WITH COMPUTER VISION DEVELOP NETWORKING APPLICATIONS USING AN OFF THE SHELF ETHERNET CHIP CREATE A PORTABLE MULTIVARIABLE GPS TRACKING AND DATA LOGGING DEVICE USE THE PROPELLER AS A REMOTE VIRTUAL PERIPHERAL FOR MEDIA APPLICATIONS CREATE A PROPELLER POWERED HVAC GREEN HOUSE MODEL SYNTHESIZE SPEECH WITH THE PROPELLER EXPERIENCE MORE OF THE PROCESS AT MHPROFESSIONAL COM PROPELLER

Programming and Customizing the Multicore Propeller Microcontroller: The Official Guide *2010-02-05*

THE ONLY OFFICIAL GUIDE TO THE PARALLAX MULTICORE PROPELLER MICROCONTROLLER WRITTEN BY A TEAM OF PROPELLER EXPERTS THIS AUTHORITATIVE GUIDE SHOWS YOU HOW TO REALIZE YOUR DESIGN CONCEPTS BY TAKING FULL ADVANTAGE OF THE MULTICORE PROPELLER MICROCONTROLLER S UNIQUE ARCHITECTURE THE BOOK BEGINS WITH A REVIEW OF THE PROPELLER HARDWARE SOFTWARE AND SPIN LANGUAGE SO YOU CAN GET STARTED RIGHT AWAY PROGRAMMING AND CUSTOMIZING THE MULTICORE PROPELLER MICROCONTROLLER THE OFFICIAL GUIDE IS FILLED WITH A WIDE VARIETY OF STEP BY STEP HANDS ON PROJECTS PUT YOUR IDEAS INTO PRODUCTION WHEN YOU LEARN HOW TO DEBUG CODE FOR MULTIPLE CORES UNDERSTAND HOW THE PROPELLER INTERACTS WITH DIFFERENT SENSORS WIRELESSLY NETWORK PROPELLER CHIPS BUILD A BALANCING ROBOT AND CONTROL IT WITH COMPUTER VISION DEVELOP NETWORKING APPLICATIONS USING AN OFF THE SHELF ETHERNET CHIP CREATE A PORTABLE MULTIVARIABLE GPS TRACKING AND DATA LOGGING DEVICE USE THE PROPELLER AS A REMOTE VIRTUAL PERIPHERAL FOR MEDIA APPLICATIONS CREATE A PROPELLER POWERED HVAC GREEN HOUSE MODEL SYNTHESIZE SPEECH WITH THE PROPELLER EXPERIENCE MORE OF THE PROCESS AT MHPROFESSIONAL COM PROPELLER

Multi-Core Embedded Systems *2018-10-08*

DETAILS A REAL WORLD PRODUCT THAT APPLIES A CUTTING EDGE MULTI CORE ARCHITECTURE INCREASINGLY DEMANDING MODERN APPLICATIONS SUCH AS THOSE USED IN TELECOMMUNICATIONS NETWORKING AND REAL TIME PROCESSING OF AUDIO VIDEO AND MULTIMEDIA STREAMS REQUIRE MULTIPLE PROCESSORS TO ACHIEVE COMPUTATIONAL PERFORMANCE AT THE RATE OF A FEW GIGA OPERATIONS PER SECOND THIS NECESSITY FOR SPEED AND MANAGEABLE POWER CONSUMPTION MAKES IT LIKELY THAT THE NEXT GENERATION OF EMBEDDED PROCESSING SYSTEMS WILL INCLUDE HUNDREDS OF CORES WHILE BEING INCREASINGLY PROGRAMMABLE BLENDING PROCESSORS AND CONFIGURABLE HARDWARE IN A POWER EFFICIENT MANNER MULTI CORE EMBEDDED SYSTEMS PRESENTS A VARIETY OF PERSPECTIVES THAT ELUCIDATE THE TECHNICAL CHALLENGES ASSOCIATED WITH SUCH INCREASED INTEGRATION OF HOMOGENEOUS PROCESSORS AND HETEROGENEOUS MULTIPLE CORES IT OFFERS AN ANALYSIS THAT INDUSTRY ENGINEERS AND PROFESSIONALS WILL NEED TO UNDERSTAND THE PHYSICAL DETAILS OF BOTH SOFTWARE AND HARDWARE IN EMBEDDED ARCHITECTURES AS WELL AS THEIR LIMITATIONS AND POTENTIAL FOR FUTURE GROWTH DISCUSSES THE AVAILABLE PROGRAMMING MODELS SPREAD ACROSS DIFFERENT ABSTRACTION LEVELS THE BOOK BEGINS WITH AN OVERVIEW OF THE EVOLUTION OF MULTIPROCESSOR ARCHITECTURES FOR EMBEDDED APPLICATIONS AND DISCUSSES TECHNIQUES FOR AUTONOMOUS POWER MANAGEMENT OF SYSTEM LEVEL PARAMETERS IT ADDRESSES THE USE OF EXISTING OPEN SOURCE AND FREE TOOLS ORIGINATING FROM SEVERAL APPLICATION DOMAINS SUCH AS TRAFFIC MODELING GRAPH THEORY PARALLEL COMPUTING AND NETWORK SIMULATION IN ADDITION THE AUTHORS COVER OTHER IMPORTANT TOPICS ASSOCIATED WITH MULTI CORE EMBEDDED SYSTEMS SUCH AS ARCHITECTURES AND INTERCONNECTS EMBEDDED DESIGN METHODOLOGIES MAPPING OF APPLICATIONS

Electronic Circuits for the Evil Genius 2/E *2010-10-22*

THE FIENDISHLY FUN WAY TO MASTER ELECTRONIC CIRCUITS FULLY UPDATED THROUGHOUT THIS WICKEDLY INVENTIVE GUIDE INTRODUCES ELECTRONIC CIRCUITS AND CIRCUIT DESIGN BOTH ANALOG AND DIGITAL THROUGH A SERIES OF PROJECTS YOU LL COMPLETE ONE SIMPLE LESSON AT A TIME THE SEPARATE LESSONS BUILD ON EACH OTHER AND ADD UP TO PROJECTS YOU CAN PUT TO PRACTICAL USE YOU DON T NEED TO KNOW ANYTHING ABOUT ELECTRONICS TO GET STARTED A PRE ASSEMBLED KIT WHICH INCLUDES ALL THE COMPONENTS AND PC BOARDS TO COMPLETE THE BOOK PROJECTS IS AVAILABLE SEPARATELY FROM ABRA ELECTRONICS ON AMAZON USING EASY TO FIND COMPONENTS AND EQUIPMENT ELECTRONIC CIRCUITS FOR THE EVIL GENIUS SECOND EDITION PROVIDES HOURS OF REWARDING AND SLIGHTLY TWISTED FUN YOU LL GAIN VALUABLE EXPERIENCE IN CIRCUIT CONSTRUCTION AND DESIGN AS YOU TEST MODIFY AND OBSERVE YOUR RESULTS SKILLS YOU CAN PUT TO WORK IN OTHER EXCITING CIRCUIT BUILDING PROJECTS ELECTRONIC CIRCUITS FOR THE EVIL GENIUS FEATURES STEP BY STEP INSTRUCTIONS AND HELPFUL ILLUSTRATIONS PROVIDES TIPS FOR

CUSTOMIZING THE PROJECTS COVERS THE UNDERLYING ELECTRONICS PRINCIPLES BEHIND THE PROJECTS REMOVES THE FRUSTRATION FACTOR ALL REQUIRED PARTS ARE LISTED ALONG WITH SOURCES BUILD THESE AND OTHER DEVIOUS DEVICES AUTOMATIC NIGHT LIGHT LIGHT SENSITIVE SWITCH ALONG TO DIGITAL CONVERTER VOLTAGE CONTROLLED OSCILLATOR OP AMP CONTROLLED POWER AMPLIFIER BURGLAR ALARM LOGIC GATE BASED TOY TWO WAY INTERCOM USING TRANSISTORS AND OP AMPS EACH FUN INEXPENSIVE GENIUS PROJECT INCLUDES A DETAILED LIST OF MATERIALS SOURCES FOR PARTS SCHEMATICS AND LOTS OF CLEAR WELL ILLUSTRATED INSTRUCTIONS FOR EASY ASSEMBLY THE LARGER WORKBOOK STYLE LAYOUT AND CONVENIENT TWO COLUMN FORMAT MAKE FOLLOWING THE STEP BY STEP INSTRUCTIONS A BREEZE MAKE GREAT STUFF TAB AN IMPRINT OF MCGRAW HILL PROFESSIONAL IS A LEADING PUBLISHER OF DIY TECHNOLOGY BOOKS FOR MAKERS HACKERS AND ELECTRONICS HOBBYISTS

tinyAVR Microcontroller Projects for the Evil Genius 2011-01-31

CREATE FIENDISHLY FUN TINYAVR MICROCONTROLLER PROJECTS THIS WICKEDLY INVENTIVE GUIDE SHOWS YOU HOW TO CONCEPTUALIZE BUILD AND PROGRAM 34 TINYAVR MICROCONTROLLER DEVICES THAT YOU CAN USE FOR EITHER ENTERTAINMENT OR PRACTICAL PURPOSES AFTER COVERING THE DEVELOPMENT PROCESS TOOLS AND POWER SUPPLY SOURCES TINYAVR MICROCONTROLLER PROJECTS FOR THE EVIL GENIUS GETS YOU WORKING ON EXCITING LED GRAPHICS LCD SENSOR AUDIO AND ALTERNATE ENERGY PROJECTS USING EASY TO FIND COMPONENTS AND EQUIPMENT THIS HANDS ON GUIDE HELPS YOU BUILD A SOLID FOUNDATION IN ELECTRONICS AND EMBEDDED PROGRAMMING WHILE ACCOMPLISHING USEFUL AND SLIGHTLY TWISTED PROJECTS MOST OF THE PROJECTS HAVE FASCINATING VISUAL APPEAL IN THE FORM OF LARGE LED BASED DISPLAYS AND OTHERS FEATURE A VOICE PLAYBACK MECHANISM FULL SOURCE CODE AND CIRCUIT FILES FOR EACH PROJECT ARE AVAILABLE FOR DOWNLOAD TINYAVR MICROCONTROLLER PROJECTS FOR THE EVIL GENIUS FEATURES STEP BY STEP INSTRUCTIONS AND HELPFUL ILLUSTRATIONS ALLOWS YOU TO CUSTOMIZE EACH PROJECT FOR YOUR OWN REQUIREMENTS OFFERS FULL SOURCE CODE FOR ALL PROJECTS FOR DOWNLOAD BUILD THESE AND OTHER DEVIOUS DEVICES FLICKERING LED CANDLE RANDOM COLOR AND MUSIC GENERATOR MOOD LAMP VU METER WITH 20 LEDS CELSIUS AND FAHRENHEIT THERMOMETER RGB DICE TENGU ON GRAPHICS DISPLAY SPINNING LED TOP WITH MESSAGE DISPLAY CONTACTLESS TACHOMETER ELECTRONIC BIRTHDAY BLOWOUT CANDLES FRIDGE ALARM MUSICAL TOY BATTERYLESS INFRARED REMOTE BATTERYLESS PERSISTENCE OF VISION TOY EACH FUN INEXPENSIVE EVIL GENIUS PROJECT INCLUDES A DETAILED LIST OF MATERIALS SOURCES FOR PARTS SCHEMATICS AND LOTS OF CLEAR WELL ILLUSTRATED INSTRUCTIONS FOR EASY ASSEMBLY THE LARGER WORKBOOK STYLE LAYOUT AND CONVENIENT TWO COLUMN FORMAT MAKE FOLLOWING THE STEP BY STEP INSTRUCTIONS A BREEZE MAKE GREAT STUFF TAB AN IMPRINT OF MCGRAW HILL PROFESSIONAL IS A LEADING PUBLISHER OF DIY TECHNOLOGY BOOKS FOR MAKERS HACKERS AND ELECTRONICS HOBBYISTS

The TAB Battery Book: An In-Depth Guide to Construction, Design, and Use 2010-12-01

SUPERCHARGE YOUR UNDERSTANDING OF BATTERY TECHNOLOGY IDEAL FOR HOBBYISTS AND ENGINEERS ALIKE THE TAB BATTERY BOOK AN IN DEPTH GUIDE TO CONSTRUCTION DESIGN AND USE OFFERS COMPREHENSIVE COVERAGE OF THESE PORTABLE ENERGY POWERHOUSES THIS PRACTICAL GUIDE DISCUSSES BATTERY CHEMISTRY AND ENGINEERING HOW BATTERIES ARE USED AND THE HISTORY OF BATTERIES YOU LL FIND OUT HOW DIFFERENT TYPES OF BATTERIES WORK AND HOW TO SELECT THE RIGHT BATTERY FOR ANY APPLICATION THE BOOK ALSO EXAMINES THE TECHNOLOGICAL ADVANCES BEING USED TO DEVELOP BATTERIES AS ROBUST ENERGY SOURCES FOR A WIDE VARIETY OF DEVICES TAP INTO THE POWER OF ALL KINDS OF BATTERIES WITH HELP FROM THIS DETAILED RESOURCE COVERAGE INCLUDES PORTABLE ENERGY AND LONG TERM ENERGY STORAGE BATTERIES FOR PORTABLE CONSUMER DEMANDS MEDICAL DEVICES ELECTRIC VEHICLES LARGE SCALE ELECTRICAL ENERGY STORAGE AND SPACE AND MILITARY APPLICATIONS BASIC PHYSICS AND CHEMISTRY THE SCIENCE OF BATTERIES CELLS ELECTROCHEMISTRY THERMODYNAMICS KINETICS AND CAPACITY BATTERY ENGINEERING DESIGNS INCLUDING ELECTRODE SEAL AND VENT DESIGN BATTERY PERFORMANCE RELIABILITY AND SAFETY PRIMARY BATTERY TECHNOLOGIES AQUEOUS AND NON AQUEOUS ELECTROLYTES INCLUDING ALKALINE AND LITHIUM RECHARGEABLE BATTERIES INCLUDING NICKEL METAL HYDRIDE AND LITHIUM ION SELECTING THE RIGHT BATTERY FOR ANY APPLICATION FUTURE TECHNOLOGIES SUCH AS THIN FILM LARGE ENERGY STORAGE AND HIGH ENERGY DENSITY BATTERIES MAKE GREAT STUFF TAB AN IMPRINT OF MCGRAW HILL PROFESSIONAL IS A LEADING PUBLISHER OF DIY TECHNOLOGY BOOKS FOR MAKERS HACKERS AND ELECTRONICS HOBBYISTS

15 Dangerously Mad Projects for the Evil Genius 2011-06-22

UNLEASH YOUR INNER MAD SCIENTIST WONDERFUL I LEARNED A LOT READING THE DETAILED BUT EASY TO UNDERSTAND INSTRUCTIONS BOINGBOING THIS WICKEDLY INVENTIVE GUIDE EXPLAINS HOW TO DESIGN AND BUILD 15 FIENDISHLY FUN ELECTRONICS PROJECTS FILLED WITH PHOTOS AND ILLUSTRATIONS 15 DANGEROUSLY MAD PROJECTS FOR THE EVIL GENIUS INCLUDES STEP BY STEP DIRECTIONS AS WELL AS A CONSTRUCTION PRIMER FOR THOSE WHO ARE NEW TO ELECTRONICS PROJECTS USING EASY TO FIND COMPONENTS AND EQUIPMENT THIS DO IT YOURSELF BOOK SHOWS YOU HOW TO CREATE A VARIETY OF MISCHIEVOUS GADGETS SUCH AS A REMOTE CONTROLLED LASER MOTORIZED MULTICOLORED LEDS THAT WRITE IN THE AIR AND A SURVEILLANCE ROBOT YOU LL ALSO LEARN TO USE THE HIGHLY POPULAR ARDUINO MICROCONTROLLER BOARD WITH THREE OF THE PROJECTS 15 DANGEROUSLY MAD PROJECTS FOR THE EVIL GENIUS FEATURES STEP BY STEP INSTRUCTIONS AND HELPFUL ILLUSTRATIONS COVERS ESSENTIAL SAFETY MEASURES REVEALS THE SCIENTIFIC PRINCIPLES BEHIND THE PROJECTS REMOVES THE FRUSTRATION FACTOR ALL REQUIRED PARTS ARE LISTED ALONG WITH SOURCES BUILD THESE DEVIOUS DEVICES TO AMAZE YOUR FRIENDS AND CONFOUND YOUR ENEMIES

COIL GUN TREBUCHET PING PONG BALL MINIGUN MINI LASER TURRET BALLOON POPPING LASER GUN TOUCH ACTIVATED LASER SIGHT LASER GRID INTRUDER ALARM PERSISTENCE OF VISION DISPLAY COVERT RADIO BUG LASER VOICE TRANSMITTER FLASH BOMB HIGH BRIGHTNESS LED STROBE LEVITATION MACHINE SNAILBOT SURVEILLANCE ROBOT EACH FUN INEXPENSIVE EVIL GENIUS PROJECT INCLUDES A DETAILED LIST OF MATERIALS SOURCES FOR PARTS SCHEMATICS AND LOTS OF CLEAR WELL ILLUSTRATED INSTRUCTIONS FOR EASY ASSEMBLY THE LARGER WORKBOOK STYLE LAYOUT AND CONVENIENT TWO COLUMN FORMAT MAKE FOLLOWING THE STEP BY STEP INSTRUCTIONS A BREEZE VIDEOS PHOTOS AND SOURCE CODE ARE AVAILABLE AT WWW DANGEROUSLYMAD COM MAKE GREAT STUFF TAB AN IMPRINT OF MCGRAW HILL PROFESSIONAL IS A LEADING PUBLISHER OF DIY TECHNOLOGY BOOKS FOR MAKERS HACKERS AND ELECTRONICS HOBBYISTS

30 Arduino Projects for the Evil Genius *2010-08-23*

30 WAYS TO HAVE SOME COMPUTER CONTROLLED EVIL FUN THE STEPS ARE EASY TO FOLLOW TEXT IS PRECISE AND UNDERSTANDABLE USES VERY CLEAR PICTURES AND SCHEMATICS TO SHOW WHAT NEEDS DOING MOST IMPORTANTLY THESE PROJECTS ARE FUN BOING BOING THIS WICKEDLY INVENTIVE GUIDE SHOWS YOU HOW TO PROGRAM AND BUILD A VARIETY OF PROJECTS WITH THE ARDUINO MICROCONTROLLER DEVELOPMENT SYSTEM COVERING WINDOWS MAC AND LINUX PLATFORMS 30 ARDUINO PROJECTS FOR THE EVIL GENIUS GETS YOU UP TO SPEED WITH THE SIMPLIFIED C PROGRAMMING YOU NEED TO KNOW NO PRIOR PROGRAMMING EXPERIENCE NECESSARY USING EASY TO FIND COMPONENTS AND EQUIPMENT THIS DO IT YOURSELF BOOK EXPLAINS HOW TO ATTACH AN ARDUINO BOARD TO YOUR COMPUTER PROGRAM IT AND CONNECT ELECTRONICS TO IT TO CREATE FIENDISHLY FUN PROJECTS THE ONLY LIMIT IS YOUR IMAGINATION 30 ARDUINO PROJECTS FOR THE EVIL GENIUS FEATURES STEP BY STEP INSTRUCTIONS AND HELPFUL ILLUSTRATIONS PROVIDES FULL SCHEMATIC AND CONSTRUCTION DETAILS FOR EVERY PROJECT COVERS THE SCIENTIFIC PRINCIPLES BEHIND THE PROJECTS REMOVES THE FRUSTRATION FACTOR ALL REQUIRED PARTS ARE LISTED ALONG WITH SOURCES BUILD THESE AND OTHER DEVIOUS DEVICES MORSE CODE TRANSLATOR HIGH POWERED STROBE LIGHT SEASONAL AFFECTIVE DISORDER LIGHT LED DICE KEYPAD SECURITY CODE PULSE RATE MONITOR USB TEMPERATURE LOGGER OSCILLOSCOPE LIGHT HARP LCD THERMOSTAT COMPUTER CONTROLLED FAN HYPNOTIZER SERVO CONTROLLED LASER LIE DETECTOR MAGNETIC DOOR LOCK INFRARED REMOTE EACH FUN INEXPENSIVE EVIL GENIUS PROJECT INCLUDES A DETAILED LIST OF MATERIALS SOURCES FOR PARTS SCHEMATICS AND LOTS OF CLEAR WELL ILLUSTRATED INSTRUCTIONS FOR EASY ASSEMBLY THE LARGER WORKBOOK STYLE LAYOUT AND CONVENIENT TWO COLUMN FORMAT MAKE FOLLOWING THE STEP BY STEP INSTRUCTIONS A BREEZE IN DECEMBER 2011 ARDUINO 1 0 WAS RELEASED THIS CHANGED A FEW THINGS THAT HAVE CAUSED THE SKETCHES FOR PROJECTS 10 27 AND 28 IN THIS BOOK TO BREAK TO FIX THIS YOU WILL NEED TO GET THE LATEST VERSIONS OF THE KEYPAD AND IRREMOTE LIBRARIES THE KEYPAD LIBRARY HAS BEEN UPDATED FOR ARDUINO 1 0 BY ITS ORIGINAL CREATORS AND CAN BE DOWNLOADED FROM HERE ARDUINO CC PLAYGROUND CODE KEYPAD KEN SHIRIFF S IRREMOTE LIBRARY HAS BEEN UPDATED AND CAN BE DOWNLOADED FROM HERE ARDUINOEVILGENIUS COM NEW DOWNLOADS MAKE GREAT STUFF TAB AN IMPRINT OF MCGRAW HILL PROFESSIONAL IS A LEADING PUBLISHER OF DIY TECHNOLOGY BOOKS FOR MAKERS HACKERS AND ELECTRONICS HOBBYISTS

Recycling Projects for the Evil Genius *2010-08-05*

HAVE SOME THOROUGHLY GREEN EVIL FUN THIS WICKEDLY INVENTIVE GUIDE EXPLAINS HOW TO CREATE A VARIETY OF PRACTICAL ENVIRONMENTALLY FRIENDLY ITEMS YOU CAN USE FOR YOURSELF OR RESELL FOR PROFIT RECYCLING PROJECTS FOR THE EVIL GENIUS IS FILLED WITH DETAILED DIRECTIONS ON HOW TO SUCCESSFULLY COMPLETE EACH GREEN PROJECT AND DISCUSSES IMPORTANT SAFETY ISSUES USING EASY TO FIND COMPONENTS AND TOOLS THIS DO IT YOURSELF BOOK SHOWS YOU HOW TO BREW UP GREEN CLEANERS TRANSFORM ALL TYPES OF PAPER INTO BUILDING MATERIALS SAFETY RID YOUR HOME AND YARD OF PESTS AND MUCH MORE ALL ON THE CHEAP RECYCLING PROJECTS FOR THE EVIL GENIUS FEATURES STEP BY STEP INSTRUCTIONS AND HELPFUL ILLUSTRATIONS COVERS ESSENTIAL SAFETY MEASURES REVEALS THE SCIENTIFIC PRINCIPLES BEHIND THE PROJECTS REMOVES THE FRUSTRATION FACTOR ALL REQUIRED PARTS ARE LISTED ALONG WITH SOURCES MAKE YOUR OWN GREEN HOUSEHOLD CLEANERS LAUNDRY SOAP CITRUS OIL EXTRACT PEST AND WEED CONTROL SOLUTIONS RECYCLED PLASTIC LUMBER AND LANDSCAPE BLOCKS RECYCLED ASPHALT SHINGLE PAVER BRICKS AND ROAD PATCH COMPOUND CONCRETE PAPER MACHE BLOCKS GARDEN WALLS STEPPING STONES AND STRUCTURES SOLAR POWERED COMPOSTER GARDEN FRIENDLY CHARCOAL AND MORE EACH FUN INEXPENSIVE AND SLIGHTLY WICKED EVIL GENIUS PROJECT INCLUDES A DETAILED LIST OF MATERIALS SOURCES FOR PARTS SCHEMATICS AND LOTS OF CLEAR WELL ILLUSTRATED INSTRUCTIONS FOR EASY ASSEMBLY THE LARGER WORKBOOK STYLE LAYOUT AND CONVENIENT TWO COLUMN FORMAT MAKE FOLLOWING THE STEP BY STEP INSTRUCTIONS A BREEZE

Arduino + Android Projects for the Evil Genius: Control Arduino with Your Smartphone or Tablet *2011-12-12*

TEAM ARDUINO UP WITH ANDROID FOR SOME MISCHIEVOUS FUN FILLED WITH PRACTICAL DO IT YOURSELF GADGETS ARDUINO ANDROID PROJECTS FOR THE EVIL GENIUS SHOWS YOU HOW TO CREATE ARDUINO DEVICES AND CONTROL THEM WITH ANDROID SMARTPHONES AND TABLETS EASY TO FIND EQUIPMENT AND COMPONENTS ARE USED FOR ALL THE PROJECTS IN THE BOOK THIS WICKEDLY INVENTIVE GUIDE COVERS THE ANDROID OPEN APPLICATION DEVELOPMENT KIT ADK AND USB INTERFACE AND EXPLAINS HOW TO USE THEM WITH THE BASIC ARDUINO PLATFORM METHODS OF COMMUNICATION BETWEEN ANDROID AND ARDUINO THAT DON T REQUIRE THE ADK INCLUDING SOUND BLUETOOTH AND WIFI ETHERNET ARE ALSO DISCUSSED AN ARDUINO ADK PROGRAMMING TUTORIAL HELPS YOU GET STARTED RIGHT AWAY ARDUINO ANDROID PROJECTS FOR THE EVIL GENIUS CONTAINS STEP BY STEP INSTRUCTIONS AND HELPFUL ILLUSTRATIONS PROVIDES TIPS FOR

CUSTOMIZING THE PROJECTS COVERS THE UNDERLYING PRINCIPLES BEHIND THE PROJECTS REMOVES THE FRUSTRATION FACTOR ALL REQUIRED PARTS ARE LISTED PROVIDES ALL SOURCE CODE ON THE BOOK S WEBSITE BUILD THESE AND OTHER DEVIOUS DEVICES BLUETOOTH ROBOT ANDROID GEIGER COUNTER ANDROID CONTROLLED LIGHT SHOW TV REMOTE TEMPERATURE LOGGER ULTRASONIC RANGE FINDER HOME AUTOMATION CONTROLLER REMOTE POWER AND LIGHTING CONTROL SMART THERMOSTAT RFID DOOR LOCK SIGNALING FLAGS DELAY TIMER

PICAXE Microcontroller Projects for the Evil Genius 2010-09-05

WHIP UP SOME FIENDISHLY FUN PICAXE MICROCONTROLLER DEVICES RON HAS WORKED HARD TO EXPLAIN HOW THE PICAXE SYSTEM OPERATES THROUGH SIMPLE EXAMPLES AND I M SURE HIS EASY TO READ STYLE WILL HELP MANY PEOPLE PROGRESS WITH THEIR PICAXE PROJECTS FROM THE FOREWORD BY CLIVE SEAGER REVOLUTION EDUCATION LTD THIS WICKEDLY INVENTIVE GUIDE SHOWS YOU HOW TO PROGRAM BUILD AND DEBUG A VARIETY OF PICAXE MICROCONTROLLER PROJECTS PICAXE MICROCONTROLLER PROJECTS FOR THE EVIL GENIUS GETS YOU STARTED WITH PROGRAMMING AND I O INTERFACING RIGHT AWAY AND THEN SHOWS YOU HOW TO DEVELOP A MASTER PROCESSOR CIRCUIT FROM HELLO WORLD TO HAIL OCTAVIUS ALL THE PROJECTS IN PART I CAN BE ACCOMPLISHED USING EITHER AN M OR M2 CLASS PICAXE PROCESSOR AND PART II ADDS 20X2 BASED MASTER PROCESSOR PROJECTS TO THE MIX PART III CULMINATES IN THE CREATION OF OCTAVIUS A SOPHISTICATED ROBOTICS EXPERIMENTATION PLATFORM FEATURING A 40X2 MASTER PROCESSOR AND EIGHT BREADBOARD STATIONS WHICH ALLOW YOU TO DEVELOP INTELLIGENT PERIPHERALS TO AUGMENT OCTAVIUS FUNCTIONING THE ONLY LIMIT IS YOUR IMAGINATION PICAXE MICROCONTROLLER PROJECTS FOR THE EVIL GENIUS FEATURES STEP BY STEP INSTRUCTIONS AND HELPFUL PHOTOS AND ILLUSTRATIONS ALLOWS YOU TO CUSTOMIZE EACH PROJECT FOR YOUR PURPOSES OFFERS ALL THE PROGRAMS IN THE BOOK FREE FOR DOWNLOAD REMOVES THE FRUSTRATION FACTOR ALL REQUIRED PARTS ARE LISTED ALONG WITH SOURCES BUILD THESE AND OTHER DEVIOUS DEVICES SIMPLE MINI STEREO JACK ADAPTER USBS PA3 PICAXE PROGRAMMING ADAPTER POWER SUPPLY THREE STATE DIGITAL LOGIC PROBE 20X2 MASTER PROCESSOR CIRCUIT TV R INPUT MODULE 8 BIT PARALLEL 16X2 LCD BOARD SERIALIZED 16X2 LCD SERIALIZED 4X4 MATRIX KEYPAD SPI 4 DIGIT LED DISPLAY COUNTDOWN TIMER PROGRAMMABLE MULTI FUNCTION PERIPHERAL DEVICE AND OPERATING SYSTEM OCTAVIUS ADVANCED ROBOTICS EXPERIMENTATION PLATFORM L298 DUAL DC MOTOR CONTROLLER BOARD EACH FUN INEXPENSIVE EVIL GENIUS PROJECT INCLUDES A DETAILED LIST OF MATERIALS SOURCES FOR PARTS SCHEMATICS AND LOTS OF CLEAR WELL ILLUSTRATED INSTRUCTIONS FOR EASY ASSEMBLY THE LARGER WORKBOOK STYLE LAYOUT AND CONVENIENT TWO COLUMN FORMAT MAKE FOLLOWING THE STEP BY STEP INSTRUCTIONS A BREEZE MAKE GREAT STUFF TAB AN IMPRINT OF MCGRAW HILL PROFESSIONAL IS A LEADING PUBLISHER OF DIY TECHNOLOGY BOOKS FOR MAKERS HACKERS AND ELECTRONICS HOBBYISTS

Scalable Multi-core Architectures 2011-10-17

AS MOORE S LAW CONTINUES TO UNFOLD TWO IMPORTANT TRENDS HAVE RECENTLY EMERGED FIRST THE GROWTH OF CHIP CAPACITY IS TRANSLATED INTO A CORRESPONDING INCREASE OF NUMBER OF CORES SECOND THE PARALLELIZATION OF THE COMPUTATION AND 3D INTEGRATION TECHNOLOGIES LEAD TO DISTRIBUTED MEMORY ARCHITECTURES THIS BOOK DESCRIBES RECENT RESEARCH THAT ADDRESSES URGENT CHALLENGES IN MANY CORE ARCHITECTURES AND APPLICATION MAPPING IT ADDRESSES THE ARCHITECTURAL DESIGN OF MANY CORE CHIPS MEMORY AND DATA MANAGEMENT POWER MANAGEMENT DESIGN AND PROGRAMMING METHODOLOGIES IT ALSO DESCRIBES HOW NEW TECHNIQUES HAVE BEEN APPLIED IN VARIOUS INDUSTRIAL CASE STUDIES

Advanced Microsystems for Automotive Applications 2016 2016-09-14

THIS BOOK CONTAINS THE PAPERS PRESENTED AT THE 20TH ANNIVERSARY EDITION OF THE AMAA CONFERENCE HELD IN BRUSSELS BELGIUM IN 2016 THE THEME OF THE CONFERENCE WAS SMART SYSTEMS FOR THE AUTOMOBILE OF THE FUTURE THE AUTOMOBILE IS CURRENTLY BEING RESHAPED AT UNPRECEDENTED PACE AUTOMATION AND ELECTRIFICATION ARE THE TWO DOMINANT MEGATRENDS WHICH DRAMATICALLY CHANGE THE CHOICE AND DESIGN OF COMPONENTS SYSTEMS VEHICULAR ARCHITECTURES AND ULTIMATELY THE WAY WE USE CARS IN THE COMING DECADES NOVEL E E ARCHITECTURES VEHICULAR CONNECTIVITY AND CLOUD SERVICES WILL BE KEY TO EXTENDING THE PERCEPTION AND DECISION MAKING HORIZONS OF AUTOMATED VEHICLES TO ENABLE COOPERATIVE FUNCTIONS AND A SEAMLESS DIGITAL USER EXPERIENCE THE AMAA S ONGOING MISSION TO DETECT NOVEL TRENDS IN AUTOMOTIVE ICT ELECTRONICS AND SMART SYSTEMS AND TO DISCUSS THE TECHNOLOGICAL IMPLICATIONS IS ONCE AGAIN REFLECTED IN THIS VOLUME THE BOOK WILL BE A VALUABLE READ FOR RESEARCH EXPERTS AND PROFESSIONALS IN THE AUTOMOTIVE AND SMART SYSTEMS INDUSTRY BUT THE BOOK MAY ALSO BE BENEFICIAL FOR GRADUATE STUDENTS

Programming Multicore and Many-core Computing Systems 2017-01-23

PROGRAMMING MULTI CORE AND MANY CORE COMPUTING SYSTEMS SABRI PLLANA LINNAEUS UNIVERSITY SWEDEN FATOS XHAFA TECHNICAL UNIVERSITY OF CATALONIA SPAIN PROVIDES STATE OF THE ART METHODS FOR PROGRAMMING MULTI CORE AND MANY CORE SYSTEMS THE BOOK COMPRISES A SELECTION OF TWENTY TWO CHAPTERS COVERING FUNDAMENTAL TECHNIQUES AND ALGORITHMS PROGRAMMING APPROACHES METHODOLOGIES AND FRAMEWORKS

SCHEDULING AND MANAGEMENT TESTING AND EVALUATION METHODOLOGIES AND CASE STUDIES FOR PROGRAMMING MULTI CORE AND MANY CORE SYSTEMS PROGRAM DEVELOPMENT FOR MULTI CORE PROCESSORS ESPECIALLY FOR HETEROGENEOUS MULTI CORE PROCESSORS IS SIGNIFICANTLY MORE COMPLEX THAN FOR SINGLE CORE PROCESSORS HOWEVER PROGRAMMERS HAVE BEEN TRADITIONALLY TRAINED FOR THE DEVELOPMENT OF SEQUENTIAL PROGRAMS AND ONLY A SMALL PERCENTAGE OF THEM HAVE EXPERIENCE WITH PARALLEL PROGRAMMING IN THE PAST ONLY A RELATIVELY SMALL GROUP OF PROGRAMMERS INTERESTED IN HIGH PERFORMANCE COMPUTING HPC WAS CONCERNED WITH THE PARALLEL PROGRAMMING ISSUES BUT THE SITUATION HAS CHANGED DRAMATICALLY WITH THE APPEARANCE OF MULTI CORE PROCESSORS ON COMMONLY USED COMPUTING SYSTEMS IT IS EXPECTED THAT WITH THE PERVASIVENESS OF MULTI CORE PROCESSORS PARALLEL PROGRAMMING WILL BECOME MAINSTREAM THE PERVASIVENESS OF MULTI CORE PROCESSORS AFFECTS A LARGE SPECTRUM OF SYSTEMS FROM EMBEDDED AND GENERAL PURPOSE TO HIGH END COMPUTING SYSTEMS THIS BOOK ASSISTS PROGRAMMERS IN MASTERING THE EFFICIENT PROGRAMMING OF MULTI CORE SYSTEMS WHICH IS OF PARAMOUNT IMPORTANCE FOR THE SOFTWARE INTENSIVE INDUSTRY TOWARDS A MORE EFFECTIVE PRODUCT DEVELOPMENT CYCLE KEY FEATURES LESSONS CHALLENGES AND ROADMAPS AHEAD CONTAINS REAL WORLD EXAMPLES AND CASE STUDIES HELPS PROGRAMMERS IN MASTERING THE EFFICIENT PROGRAMMING OF MULTI CORE AND MANY CORE SYSTEMS THE BOOK SERVES AS A REFERENCE FOR A LARGER AUDIENCE OF PRACTITIONERS YOUNG RESEARCHERS AND GRADUATE LEVEL STUDENTS A BASIC LEVEL OF PROGRAMMING KNOWLEDGE IS REQUIRED TO USE THIS BOOK

VLSI 2010 Annual Symposium 2011-09-08

VLSI 2010 ANNUAL SYMPOSIUM WILL PRESENT EXTENDED VERSIONS OF THE BEST PAPERS PRESENTED IN ISVLSI 2010 CONFERENCE THE AREAS COVERED BY THE PAPERS WILL INCLUDE AMONG OTHERS EMERGING TRENDS IN VLSI NANOELECTRONICS MOLECULAR BIOLOGICAL AND QUANTUM COMPUTING MEMS VLSI CIRCUITS AND SYSTEMS FIELD PROGRAMMABLE AND RECONFIGURABLE SYSTEMS SYSTEM LEVEL DESIGN SYSTEM ON A CHIP DESIGN APPLICATION SPECIFIC LOW POWER VLSI SYSTEM DESIGN SYSTEM ISSUES IN COMPLEXITY LOW POWER HEAT DISSIPATION POWER AWARENESS IN VLSI DESIGN TEST AND VERIFICATION MIXED SIGNAL DESIGN AND ANALYSIS ELECTRICAL PACKAGING CO DESIGN PHYSICAL DESIGN INTELLECTUAL PROPERTY CREATING AND SHARING

Hardware/Software Co-design for Heterogeneous Multi-core Platforms 2012-02-02

HW SW CO DESIGN FOR HETEROGENEOUS MULTI CORE PLATFORMS DESCRIBES THE RESULTS AND OUTCOME OF THE FP6 PROJECT WHICH FOCUSES ON THE DEVELOPMENT OF AN INTEGRATED TOOL CHAIN TARGETING A HETEROGENEOUS MULTI CORE PLATFORM COMPRISING OF A GENERAL PURPOSE PROCESSOR ARM OR POWERPC A DSP THE DIOPSIS AND AN FPGA THE TOOL CHAIN TAKES EXISTING SOURCE CODE AND PROPOSES TRANSFORMATIONS AND MAPPINGS SUCH THAT LEGACY CODE CAN EASILY BE PORTED TO A MODERN MULTI CORE PLATFORM DOWNLOADABLE SOFTWARE WILL BE PROVIDED FOR SIMULATION PURPOSES

Software Development for Embedded Multi-core Systems 2011-04-08

THE MULTICORE REVOLUTION HAS REACHED THE DEPLOYMENT STAGE IN EMBEDDED SYSTEMS RANGING FROM SMALL ULTRAMOBILE DEVICES TO LARGE TELECOMMUNICATION SERVERS THE TRANSITION FROM SINGLE TO MULTICORE PROCESSORS MOTIVATED BY THE NEED TO INCREASE PERFORMANCE WHILE CONSERVING POWER HAS PLACED GREAT RESPONSIBILITY ON THE SHOULDERS OF SOFTWARE ENGINEERS IN THIS NEW EMBEDDED MULTICORE ERA THE TOUGHEST TASK IS THE DEVELOPMENT OF CODE TO SUPPORT MORE SOPHISTICATED SYSTEMS THIS BOOK PROVIDES EMBEDDED ENGINEERS WITH SOLID GROUNDING IN THE SKILLS REQUIRED TO DEVELOP SOFTWARE TARGETING MULTICORE PROCESSORS WITHIN THE TEXT THE AUTHOR UNDERTAKES AN IN DEPTH EXPLORATION OF PERFORMANCE ANALYSIS AND A CLOSE UP LOOK AT THE TOOLS OF THE TRADE BOTH GENERAL MULTICORE DESIGN PRINCIPLES AND PROCESSOR SPECIFIC OPTIMIZATION TECHNIQUES ARE REVEALED DETAILED COVERAGE OF CRITICAL ISSUES FOR MULTICORE EMPLOYMENT WITHIN EMBEDDED SYSTEMS IS PROVIDED INCLUDING THE THREADING DEVELOPMENT CYCLE WITH DISCUSSIONS OF ANALYSIS DESIGN DEVELOPMENT DEBUGGING AND PERFORMANCE TUNING OF THREADED APPLICATIONS SOFTWARE DEVELOPMENT TECHNIQUES ENGENDERING OPTIMAL MOBILITY AND ENERGY EFFICIENCY ARE HIGHLIGHTED THROUGH MULTIPLE CASE STUDIES WHICH PROVIDE PRACTICAL HOW TO ADVICE ON IMPLEMENTING THE LATEST MULTICORE PROCESSORS FINALLY FUTURE TRENDS ARE DISCUSSED INCLUDING TERASCALE SPECULATIVE MULTITHREADING TRANSACTIONAL MEMORY INTERCONNECTS AND THE SOFTWARE SPECIFIC IMPLICATIONS OF THESE LOOMING ARCHITECTURAL DEVELOPMENTS TABLE OF CONTENTS CHAPTER 1 INTRODUCTION CHAPTER 2 BASIC SYSTEM AND PROCESSOR ARCHITECTURE CHAPTER 3 MULTI CORE PROCESSORS EMBEDDED CHAPTER 4 MOVING TO MULTI CORE INTEL ARCHITECTURE CHAPTER 5 SCALAR OPTIMIZATION USABILITY CHAPTER 6 PARALLEL OPTIMIZATION USING THREADS CHAPTER 7 CASE STUDY DATA DECOMPOSITION CHAPTER 8 CASE STUDY FUNCTIONAL DECOMPOSITION CHAPTER 9 VIRTUALIZATION PARTITIONING CHAPTER 10 GETTING READY FOR LOW POWER INTEL ARCHITECTURE CHAPTER 11 SUMMARY TRENDS AND CONCLUSIONS APPENDIX I GLOSSARY REFERENCES THIS IS THE ONLY BOOK TO EXPLAIN SOFTWARE OPTIMIZATION FOR EMBEDDED MULTI CORE SYSTEMS HELPFUL TIPS TRICKS AND DESIGN SECRETS FROM AN INTEL PROGRAMMING EXPERT WITH DETAILED EXAMPLES USING THE POPULAR X86 ARCHITECTURE COVERS HOT TOPICS INCLUDING ULTRAMOBILE DEVICES LOW POWER DESIGNS PTHREADS VS OPENMP AND HETEROGENEOUS CORES

Large Scale Network-Centric Distributed Systems 2013-10-10

A HIGHLY ACCESSIBLE REFERENCE OFFERING A BROAD RANGE OF TOPICS AND INSIGHTS ON LARGE SCALE NETWORK CENTRIC DISTRIBUTED SYSTEMS EVOLVING FROM THE FIELDS OF HIGH PERFORMANCE COMPUTING AND NETWORKING LARGE SCALE NETWORK CENTRIC DISTRIBUTED SYSTEMS CONTINUES TO GROW AS ONE OF THE MOST IMPORTANT TOPICS IN COMPUTING AND COMMUNICATION AND MANY INTERDISCIPLINARY AREAS DEALING WITH BOTH WIRED AND WIRELESS NETWORKS THIS BOOK FOCUSES ON THE DESIGN AND PERFORMANCE ISSUES OF SUCH SYSTEMS LARGE SCALE NETWORK CENTRIC DISTRIBUTED SYSTEMS PROVIDES IN DEPTH COVERAGE RANGING FROM GROUND LEVEL HARDWARE ISSUES SUCH AS BUFFER ORGANIZATION ROUTER DELAY AND FLOW CONTROL TO THE HIGH LEVEL ISSUES IMMEDIATELY CONCERNING APPLICATION OR SYSTEM USERS INCLUDING PARALLEL PROGRAMMING MIDDLEWARE AND OS SUPPORT FOR SUCH COMPUTING SYSTEMS ARRANGED IN FIVE PARTS IT EXPLAINS AND ANALYZES COMPLEX TOPICS TO AN UNPRECEDENTED DEGREE PART 1 MULTICORE AND MANY CORE MC SYSTEMS ON CHIP PART 2 PERVASIVE UBIQUITOUS COMPUTING AND PEER TO PEER SYSTEMS PART 3 WIRELESS MOBILE NETWORKS PART 4 GRID AND CLOUD COMPUTING PART 5 OTHER TOPICS RELATED TO NETWORK CENTRIC COMPUTING AND ITS APPLICATIONS LARGE SCALE NETWORK CENTRIC DISTRIBUTED SYSTEMS IS AN INCREDIBLY USEFUL RESOURCE FOR PRACTITIONERS POSTGRADUATE STUDENTS POSTDOCS AND RESEARCHERS

Design of Cost-Efficient Interconnect Processing Units 2020-10-14

STREAMLINED DESIGN SOLUTIONS SPECIFICALLY FOR NOC TO SOLVE CRITICAL NETWORK ON CHIP NOC ARCHITECTURE AND DESIGN PROBLEMS RELATED TO STRUCTURE PERFORMANCE AND MODULARITY ENGINEERS GENERALLY RELY ON GUIDANCE FROM THE ABUNDANCE OF LITERATURE ABOUT BETTER UNDERSTOOD SYSTEM LEVEL INTERCONNECTION NETWORKS HOWEVER ON CHIP NETWORKS PRESENT SEVERAL DISTINCT CHALLENGES THAT REQUIRE NOVEL AND SPECIALIZED SOLUTIONS NOT FOUND IN THE TRIED AND TRUE SYSTEM LEVEL TECHNIQUES A BALANCED ANALYSIS OF NOC ARCHITECTURE AS THE FIRST DETAILED DESCRIPTION OF THE COMMERCIAL SPIDERGON STNOC ARCHITECTURE DESIGN OF COST EFFICIENT INTERCONNECT PROCESSING UNITS SPIDERGON STNOC EXAMINES THE HIGHLY REGARDED COST CUTTING TECHNOLOGY THAT IS SET TO REPLACE WELL KNOWN SHARED BUS ARCHITECTURES SUCH AS STBUS FOR DEMANDING MULTIPROCESSOR SYSTEM ON CHIP SOC APPLICATIONS EMPLOYING A BALANCED WELL ORGANIZED STRUCTURE SIMPLE TEACHING METHODS NUMEROUS ILLUSTRATIONS AND EASY TO UNDERSTAND EXAMPLES THE AUTHORS EXPLAIN HOW THE SOC AND NOC TECHNOLOGY WORKS WHY DEVELOPERS DESIGNED IT THE WAY THEY DID THE SYSTEM LEVEL DESIGN METHODOLOGY AND TOOLS USED TO CONFIGURE THE SPIDERGON STNOC ARCHITECTURE DIFFERENCES IN COST STRUCTURE BETWEEN NOCS AND SYSTEM LEVEL NETWORKS FROM PROFESSIONALS IN COMPUTER SCIENCES ELECTRICAL ENGINEERING AND OTHER RELATED FIELDS TO SEMICONDUCTOR VENDORS AND INVESTORS ALL READERS WILL APPRECIATE THE ENCYCLOPEDIC TREATMENT OF BACKGROUND NOC INFORMATION RANGING FROM CMPS TO THE BASICS OF INTERCONNECTION NETWORKS THE TEXT INTRODUCES INNOVATIVE SYSTEM LEVEL DESIGN METHODOLOGY AND TOOLS FOR EFFICIENT DESIGN SPACE EXPLORATION AND TOPOLOGY SELECTION IT ALSO PROVIDES A WEALTH OF KEY THEORETICAL AND PRACTICAL MPSOC AND NOC TOPICS SUCH AS TECHNOLOGICAL DEEP SUB MICRON EFFECTS HOMOGENEOUS AND HETEROGENEOUS PROCESSOR ARCHITECTURES MULTICORE SOC INTERCONNECT PROCESSING UNITS GENERIC NOC COMPONENTS AND EMBEDDINGS OF COMMON COMMUNICATION PATTERNS

Modeling and Optimization of Parallel and Distributed Embedded Systems 2016-02-08

THIS BOOK INTRODUCES THE STATE OF THE ART IN RESEARCH IN PARALLEL AND DISTRIBUTED EMBEDDED SYSTEMS WHICH HAVE BEEN ENABLED BY DEVELOPMENTS IN SILICON TECHNOLOGY MICRO ELECTRO MECHANICAL SYSTEMS MEMS WIRELESS COMMUNICATIONS COMPUTER NETWORKING AND DIGITAL ELECTRONICS THESE SYSTEMS HAVE DIVERSE APPLICATIONS IN DOMAINS INCLUDING MILITARY AND DEFENSE MEDICAL AUTOMOTIVE AND UNMANNED AUTONOMOUS VEHICLES THE EMPHASIS OF THE BOOK IS ON THE MODELING AND OPTIMIZATION OF EMERGING PARALLEL AND DISTRIBUTED EMBEDDED SYSTEMS IN RELATION TO THE THREE KEY DESIGN METRICS OF PERFORMANCE POWER AND DEPENDABILITY KEY FEATURES INCLUDES AN EMBEDDED WIRELESS SENSOR NETWORKS CASE STUDY TO HELP ILLUSTRATE THE MODELING AND OPTIMIZATION OF DISTRIBUTED EMBEDDED SYSTEMS PROVIDES AN ANALYSIS OF MULTI CORE MANY CORE BASED EMBEDDED SYSTEMS TO EXPLAIN THE MODELING AND OPTIMIZATION OF PARALLEL EMBEDDED SYSTEMS FEATURES AN APPLICATION METRICS ESTIMATION MODEL MARKOV MODELING FOR FAULT TOLERANCE AND ANALYSIS AND QUEUEING THEORETIC MODELING FOR PERFORMANCE EVALUATION DISCUSSES OPTIMIZATION APPROACHES FOR DISTRIBUTED WIRELESS SENSOR NETWORKS HIGH PERFORMANCE AND ENERGY EFFICIENT TECHNIQUES AT THE ARCHITECTURE MIDDLEWARE AND SOFTWARE LEVELS FOR PARALLEL MULTICORE BASED EMBEDDED SYSTEMS AND DYNAMIC OPTIMIZATION METHODOLOGIES HIGHLIGHTS RESEARCH CHALLENGES AND FUTURE RESEARCH DIRECTIONS THE BOOK IS PRIMARILY AIMED AT RESEARCHERS IN EMBEDDED SYSTEMS HOWEVER IT WILL ALSO SERVE AS AN INVALUABLE REFERENCE TO SENIOR UNDERGRADUATE AND GRADUATE STUDENTS WITH AN INTEREST IN EMBEDDED SYSTEMS RESEARCH

Computer Architecture 2006-11-03

THE ERA OF SEEMINGLY UNLIMITED GROWTH IN PROCESSOR PERFORMANCE IS OVER SINGLE CHIP ARCHITECTURES CAN NO LONGER OVERCOME THE PERFORMANCE LIMITATIONS IMPOSED BY THE POWER THEY CONSUME AND THE HEAT THEY GENERATE TODAY INTEL AND OTHER SEMICONDUCTOR FIRMS ARE ABANDONING THE SINGLE FAST PROCESSOR MODEL IN

FAVOR OF MULTI CORE MICROPROCESSORS CHIPS THAT COMBINE TWO OR MORE PROCESSORS IN A SINGLE PACKAGE IN THE FOURTH EDITION OF COMPUTER ARCHITECTURE THE AUTHORS FOCUS ON THIS HISTORIC SHIFT INCREASING THEIR COVERAGE OF MULTIPROCESSORS AND EXPLORING THE MOST EFFECTIVE WAYS OF ACHIEVING PARALLELISM AS THE KEY TO UNLOCKING THE POWER OF MULTIPLE PROCESSOR ARCHITECTURES ADDITIONALLY THE NEW EDITION HAS EXPANDED AND UPDATED COVERAGE OF DESIGN TOPICS BEYOND PROCESSOR PERFORMANCE INCLUDING POWER RELIABILITY AVAILABILITY AND DEPENDABILITY CD SYSTEM REQUIREMENTS PDF VIEWER THE CD MATERIAL INCLUDES PDF DOCUMENTS THAT YOU CAN READ WITH A PDF VIEWER SUCH AS ADOBE ACROBAT OR ADOBE READER RECENT VERSIONS OF ADOBE READER FOR SOME PLATFORMS ARE INCLUDED ON THE CD HTML BROWSER THE NAVIGATION FRAMEWORK ON THIS CD IS DELIVERED IN HTML AND JAVASCRIPT IT IS RECOMMENDED THAT YOU INSTALL THE LATEST VERSION OF YOUR FAVORITE HTML BROWSER TO VIEW THIS CD THE CONTENT HAS BEEN VERIFIED UNDER WINDOWS XP WITH THE FOLLOWING BROWSERS INTERNET EXPLORER 6 0 FIREFOX 1 5 UNDER MAC OS X PANTHER WITH THE FOLLOWING BROWSERS INTERNET EXPLORER 5 2 FIREFOX 1 0 6 SAFARI 1 3 AND UNDER MANDRIVA LINUX 2006 WITH THE FOLLOWING BROWSERS FIREFOX 1 0 6 KONQUEROR 3 4 2 MOZILLA 1 7 11 THE CONTENT IS DESIGNED TO BE VIEWED IN A BROWSER WINDOW THAT IS AT LEAST 720 PIXELS WIDE YOU MAY FIND THE CONTENT DOES NOT DISPLAY WELL IF YOUR DISPLAY IS NOT SET TO AT LEAST 1024x768 PIXEL RESOLUTION OPERATING SYSTEM THIS CD CAN BE USED UNDER ANY OPERATING SYSTEM THAT INCLUDES AN HTML BROWSER AND A PDF VIEWER THIS INCLUDES WINDOWS MAC OS AND MOST LINUX AND UNIX SYSTEMS INCREASED COVERAGE ON ACHIEVING PARALLELISM WITH MULTIPROCESSORS CASE STUDIES OF LATEST TECHNOLOGY FROM INDUSTRY INCLUDING THE SUN NIAGARA MULTIPROCESSOR AMD OPTERON AND PENTIUM 4 THREE REVIEW APPENDICES INCLUDED IN THE PRINTED VOLUME REVIEW THE BASIC AND INTERMEDIATE PRINCIPLES THE MAIN TEXT RELIES UPON EIGHT REFERENCE APPENDICES COLLECTED ON THE CD COVER A RANGE OF TOPICS INCLUDING SPECIFIC ARCHITECTURES EMBEDDED SYSTEMS APPLICATION SPECIFIC PROCESSORS SOME GUEST AUTHORED BY SUBJECT EXPERTS

ADVANCES IN COMPUTERS *2011-09-21*

THE SERIES COVERS NEW DEVELOPMENTS IN COMPUTER TECHNOLOGY MOST CHAPTERS PRESENT AN OVERVIEW OF A CURRENT SUBFIELD WITHIN COMPUTERS WITH MANY CITATIONS AND OFTEN INCLUDE NEW DEVELOPMENTS IN THE FIELD BY THE AUTHORS OF THE INDIVIDUAL CHAPTERS TOPICS INCLUDE HARDWARE SOFTWARE THEORETICAL UNDERPINNINGS OF COMPUTING AND NOVEL APPLICATIONS OF COMPUTERS THIS CURRENT VOLUME EMPHASIZES ARCHITECTURAL ADVANCES AND INCLUDES FIVE CHAPTERS ON HARDWARE DEVELOPMENT GAMES FOR MOBILE DEVICES SUCH AS CELL PHONES AND OPEN SOURCE SOFTWARE DEVELOPMENT THE BOOK SERIES IS A VALUABLE ADDITION TO UNIVERSITY COURSES THAT EMPHASIZE THE TOPICS UNDER DISCUSSION IN THAT PARTICULAR VOLUME AS WELL AS BELONGING ON THE BOOKSHELF OF INDUSTRIAL PRACTITIONERS WHO NEED TO IMPLEMENT MANY OF THE TECHNOLOGIES THAT ARE DESCRIBED CURRENT INFORMATION ON POWER REQUIREMENTS FOR NEW PROCESSORS DEVELOPMENT OF GAMES FOR DEVICES WITH LIMITED SCREEN SIZES E G CELLULAR TELEPHONES OPEN SOURCE SOFTWARE DEVELOPMENT MULTICORE PROCESSORS

EURO-PAR 2013: PARALLEL PROCESSING *2013-07-20*

THIS BOOK CONSTITUTES THE REFEREED PROCEEDINGS OF THE 19TH INTERNATIONAL CONFERENCE ON PARALLEL AND DISTRIBUTED COMPUTING EURO PAR 2013 HELD IN AACHEN GERMANY IN AUGUST 2013 THE 70 REVISED FULL PAPERS PRESENTED WERE CAREFULLY REVIEWED AND SELECTED FROM 261 SUBMISSIONS THE PAPERS ARE ORGANIZED IN 16 TOPICAL SECTIONS SUPPORT TOOLS AND ENVIRONMENTS PERFORMANCE PREDICTION AND EVALUATION SCHEDULING AND LOAD BALANCING HIGH PERFORMANCE ARCHITECTURES AND COMPILERS PARALLEL AND DISTRIBUTED DATA MANAGEMENT GRID CLUSTER AND CLOUD COMPUTING PEER TO PEER COMPUTING DISTRIBUTED SYSTEMS AND ALGORITHMS PARALLEL AND DISTRIBUTED PROGRAMMING PARALLEL NUMERICAL ALGORITHMS MULTICORE AND MANYCORE PROGRAMMING THEORY AND ALGORITHMS FOR PARALLEL COMPUTATION HIGH PERFORMANCE NETWORKS AND COMMUNICATION HIGH PERFORMANCE AND SCIENTIFIC APPLICATIONS GPU AND ACCELERATOR COMPUTING AND EXTREME SCALE COMPUTING

PRO NETBEANS IDE 6 RICH CLIENT PLATFORM EDITION *2008-03-28*

THIS BOOK WILL ENABLE YOU TO RAPIDLY DEVELOP JAVA FRONT ENDS OF APPLICATIONS USING API BUTTONS FUNCTIONS AND FEATURES MOSTLY BASED IN THE JAVA SE 6 PLATFORM IT COVERS WORKING WITH RICH CLIENT PLATFORM FEATURES AVAILABLE IN NETBEANS FOR BUILDING WEB BASED APPLICATION FRONT ENDS THE BOOK ALSO SHOWS YOU THE WEB TIER DEVELOPMENT PLUG INS AVAILABLE INCLUDING JSF STRUTS ACTION FRAMEWORK 2 0 AND JRUBY FOR BRINGING THE RUBY ON RAILS FEATURE SET TO JAVA VIA NETBEANS

NETWORKS-ON-CHIP *2014-12-04*

NETWORKS ON CHIP FROM IMPLEMENTATIONS TO PROGRAMMING PARADIGMS PROVIDES A THOROUGH AND BOTTOM UP EXPLORATION OF THE WHOLE NOC DESIGN SPACE IN A COHERENT AND UNIFORM FASHION FROM LOW LEVEL ROUTER BUFFER AND TOPOLOGY IMPLEMENTATIONS TO ROUTING AND FLOW CONTROL SCHEMES TO CO OPTIMIZATIONS OF NOC AND HIGH LEVEL PROGRAMMING PARADIGMS THIS TEXTBOOK IS INTENDED FOR AN ADVANCED COURSE ON COMPUTER ARCHITECTURE SUITABLE FOR GRADUATE STUDENTS OR SENIOR UNDERGRADS WHO WANT TO SPECIALIZE IN THE AREA OF COMPUTER ARCHITECTURE AND NETWORKS ON CHIP IT IS ALSO INTENDED FOR PRACTITIONERS IN THE INDUSTRY IN THE AREA OF MICROPROCESSOR DESIGN ESPECIALLY THE MANY CORE PROCESSOR DESIGN WITH A NETWORK ON CHIP GRADUATES CAN LEARN MANY PRACTICAL AND THEORETICAL LESSONS FROM THIS COURSE AND ALSO CAN BE MOTIVATED TO DELVE

FURTHER INTO THE IDEAS AND DESIGNS PROPOSED IN THIS BOOK INDUSTRIAL ENGINEERS CAN REFER TO THIS BOOK TO MAKE PRACTICAL TRADEOFFS AS WELL GRADUATES AND ENGINEERS WHO FOCUS ON OFF CHIP NETWORK DESIGN CAN ALSO REFER TO THIS BOOK TO ACHIEVE DEADLOCK FREE ROUTING ALGORITHM DESIGNS PROVIDES THOROUGH AND INSIGHTFUL EXPLORATION OF NOC DESIGN SPACE DESCRIPTION FROM LOW LEVEL LOGIC IMPLEMENTATIONS TO CO OPTIMIZATIONS OF HIGH LEVEL PROGRAM PARADIGMS AND NOCS THE COHERENT AND UNIFORM FORMAT OFFERS READERS A CLEAR QUICK AND EFFICIENT EXPLORATION OF NOC DESIGN SPACE COVERS MANY NOVEL AND EXCITING RESEARCH IDEAS WHICH ENCOURAGE RESEARCHERS TO FURTHER DELVE INTO THESE TOPICS PRESENTS BOTH ENGINEERING AND THEORETICAL CONTRIBUTIONS THE DETAILED DESCRIPTION OF THE ROUTER BUFFER AND TOPOLOGY IMPLEMENTATIONS COMPARISONS AND ANALYSIS ARE OF HIGH ENGINEERING VALUE

EURO-PAR 2014: PARALLEL PROCESSING WORKSHOPS *2014-12-11*

THE TWO VOLUMES LNCS 8805 AND 8806 CONSTITUTE THE THOROUGHLY REFEREED POST CONFERENCE PROCEEDINGS OF 18 WORKSHOPS HELD AT THE 20TH INTERNATIONAL CONFERENCE ON PARALLEL COMPUTING EURO PAR 2014 IN PORTO PORTUGAL IN AUGUST 2014 THE 100 REVISED FULL PAPERS PRESENTED WERE CAREFULLY REVIEWED AND SELECTED FROM 173 SUBMISSIONS THE VOLUMES INCLUDE PAPERS FROM THE FOLLOWING WORKSHOPS APCI E FIRST WORKSHOP ON APPLICATIONS OF PARALLEL COMPUTATION IN INDUSTRY AND ENGINEERING BIGDATACLOUD THIRD WORKSHOP ON BIG DATA MANAGEMENT IN CLOUDS DIHC SECOND WORKSHOP ON DEPENDABILITY AND INTEROPERABILITY IN HETEROGENEOUS CLOUDS FEDICI SECOND WORKSHOP ON FEDERATIVE AND INTEROPERABLE CLOUD INFRASTRUCTURES HETERO PAR 12TH INTERNATIONAL WORKSHOP ON ALGORITHMS MODELS AND TOOLS FOR PARALLEL COMPUTING ON HETEROGENEOUS PLATFORMS HIBB 5TH WORKSHOP ON HIGH PERFORMANCE BIOINFORMATICS AND BIOMEDICINE LSDVE SECOND WORKSHOP ON LARGE SCALE DISTRIBUTED VIRTUAL ENVIRONMENTS ON CLOUDS AND P2P MUCOCOS 7TH INTERNATIONAL WORKSHOP ON MULTI MANY CORE COMPUTING SYSTEMS OMHI THIRD WORKSHOP ON ON CHIP MEMORY HIERARCHIES AND INTERCONNECTS PADAPS SECOND WORKSHOP ON PARALLEL AND DISTRIBUTED AGENT BASED SIMULATIONS PROPER 7TH WORKSHOP ON PRODUCTIVITY AND PERFORMANCE RESILIENCE 7TH WORKSHOP ON RESILIENCY IN HIGH PERFORMANCE COMPUTING WITH CLUSTERS CLOUDS AND GRIDS REPPAR FIRST INTERNATIONAL WORKSHOP ON REPRODUCIBILITY IN PARALLEL COMPUTING ROME SECOND WORKSHOP ON RUNTIME AND OPERATING SYSTEMS FOR THE MANY CORE ERA SPPEXA WORKSHOP ON SOFTWARE FOR EXASCALE COMPUTING TASUS FIRST WORKSHOP ON TECHNIQUES AND APPLICATIONS FOR SUSTAINABLE ULTRASCALE COMPUTING SYSTEMS UCHPC 7TH WORKSHOP ON UN CONVENTIONAL HIGH PERFORMANCE COMPUTING AND VHPC 9TH WORKSHOP ON VIRTUALIZATION IN HIGH PERFORMANCE CLOUD COMPUTING

EMBEDDED ANDROID *2013-03-15*

EMBEDDED ANDROID IS FOR DEVELOPERS WANTING TO CREATE EMBEDDED SYSTEMS BASED ON ANDROID AND FOR THOSE WANTING TO PORT ANDROID TO NEW HARDWARE OR CREATING A CUSTOM DEVELOPMENT ENVIRONMENT HACKERS AND MODERS WILL ALSO FIND THIS AN INDISPENSIBLE GUIDE TO HOW ANDROID WORKS

PROCESS ALGEBRA FOR PARALLEL AND DISTRIBUTED PROCESSING *2008-12-22*

COLLECTS THE LATEST RESEARCH INVOLVING THE APPLICATION OF PROCESS ALGEBRA TO COMPUTINGEXPLORING STATE OF THE ART APPLICATIONS PROCESS ALGEBRA FOR PARALLEL AND DISTRIBUTED PROCESSING SHOWS HOW ONE FORMAL METHOD OF REASONING PROCESS ALGEBRA HAS BECOME A POWERFUL TOOL FOR SOLVING DESIGN AND IMPLEMENTATION CHALLENGES OF CONCURRENT SYSTEMS PARALLEL PR

EMBEDDED COMPUTING FOR HIGH PERFORMANCE *2017-06-13*

EMBEDDED COMPUTING FOR HIGH PERFORMANCE DESIGN EXPLORATION AND CUSTOMIZATION USING HIGH LEVEL COMPILATION AND SYNTHESIS TOOLS PROVIDES A SET OF REAL LIFE EXAMPLE IMPLEMENTATIONS THAT MIGRATE TRADITIONAL DESKTOP SYSTEMS TO EMBEDDED SYSTEMS WORKING WITH POPULAR HARDWARE INCLUDING XILINX AND ARM THE BOOK OFFERS A COMPREHENSIVE DESCRIPTION OF TECHNIQUES FOR MAPPING COMPUTATIONS EXPRESSED IN PROGRAMMING LANGUAGES SUCH AS C OR MATLAB TO HIGH PERFORMANCE EMBEDDED ARCHITECTURES CONSISTING OF MULTIPLE CPUS GPUS AND RECONFIGURABLE HARDWARE FPGAS THE AUTHORS DEMONSTRATE A DOMAIN SPECIFIC LANGUAGE LARA THAT FACILITATES RETARGETING TO MULTIPLE COMPUTING SYSTEMS USING THE SAME SOURCE CODE IN THIS WAY USERS CAN DECOUPLE ORIGINAL APPLICATION CODE FROM TRANSFORMED CODE AND ENHANCE PRODUCTIVITY AND PROGRAM PORTABILITY AFTER READING THIS BOOK ENGINEERS WILL UNDERSTAND THE PROCESSES METHODOLOGIES AND BEST PRACTICES NEEDED FOR THE DEVELOPMENT OF APPLICATIONS FOR HIGH PERFORMANCE EMBEDDED COMPUTING SYSTEMS FOCUSES ON MAXIMIZING PERFORMANCE WHILE MANAGING ENERGY CONSUMPTION IN EMBEDDED SYSTEMS EXPLAINS HOW TO RETARGET CODE FOR HETEROGENEOUS SYSTEMS WITH GPUS AND FPGAS DEMONSTRATES A DOMAIN SPECIFIC LANGUAGE THAT FACILITATES MIGRATING AND RETARGETING EXISTING APPLICATIONS TO MODERN SYSTEMS INCLUDES DOWNLOADABLE SLIDES TOOLS AND TUTORIALS

The Dark Side of Silicon 2016-12-31

THIS BOOK PRESENTS THE STATE OF THE ART OF ONE OF THE MAIN CONCERNS WITH MICROPROCESSORS TODAY A PHENOMENON KNOWN AS DARK SILICON READERS WILL LEARN HOW POWER CONSTRAINTS BOTH LEAKAGE AND DYNAMIC POWER LIMIT THE EXTENT TO WHICH LARGE PORTIONS OF A CHIP CAN BE POWERED UP AT A GIVEN TIME I E HOW MUCH ACTUAL PERFORMANCE AND FUNCTIONALITY THE MICROPROCESSOR CAN PROVIDE THE AUTHORS DESCRIBE THEIR RESEARCH TOWARD THE FUTURE OF MICROPROCESSOR DEVELOPMENT IN THE DARK SILICON ERA COVERING A VARIETY OF IMPORTANT ASPECTS OF DARK SILICON AWARE ARCHITECTURES INCLUDING DESIGN MANAGEMENT RELIABILITY AND TEST READERS WILL BENEFIT FROM SPECIFIC RECOMMENDATIONS FOR MITIGATING THE DARK SILICON PHENOMENON INCLUDING ENERGY EFFICIENT DEDICATED SOLUTIONS AND TECHNOLOGIES TO MAXIMIZE THE UTILIZATION AND RELIABILITY OF MICROPROCESSORS

Big Book of Windows Hacks 2007

THIS USEFUL BOOK GIVES WINDOWS POWER USERS EVERYTHING THEY NEED TO GET THE MOST OUT OF THEIR OPERATING SYSTEM ITS RELATED APPLICATIONS AND ITS HARDWARE

Next-Generation Business Intelligence Software with Silverlight 3 2011-02-02

BUSINESS INTELLIGENCE BI SOFTWARE IS THE CODE AND TOOLS THAT ALLOW YOU TO VIEW DIFFERENT COMPONENTS OF A BUSINESS USING A SINGLE VISUAL PLATFORM MAKING COMPREHENDING MOUNTAINS OF DATA EASIER APPLICATIONS THAT INCLUDE REPORTS ANALYTICS STATISTICS AND HISTORICAL AND PREDICTIVE MODELING ARE ALL EXAMPLES OF BI APPLICATIONS CURRENTLY WE ARE IN THE SECOND GENERATION OF BI SOFTWARE CALLED BI 2 0 THIS GENERATION IS FOCUSED ON WRITING BI SOFTWARE THAT IS PREDICTIVE ADAPTIVE SIMPLE AND INTERACTIVE AS COMPUTERS AND SOFTWARE HAVE EVOLVED MORE DATA CAN BE PRESENTED TO END USERS WITH INCREASINGLY VISUALLY RICH TECHNIQUES RICH INTERNET APPLICATION RIA TECHNOLOGIES SUCH AS MICROSOFT SILVERLIGHT CAN BE USED TO TRANSFORM TRADITIONAL USER INTERFACES FILLED WITH BORING DATA INTO FULLY INTERACTIVE ANALYTICAL APPLICATIONS TO DELIVER INSIGHT FROM LARGE DATA SETS QUICKLY FURTHERMORE RIAS INCLUDE 3D SPATIAL DESIGN CAPABILITIES THAT ALLOW FOR INTERESTING LAYOUTS OF AGGREGATED DATA BEYOND A SIMPLE LIST OR GRID BI 2 0 IMPLEMENTED VIA RIA TECHNOLOGY CAN TRULY BRING OUT THE POWER OF BI AND DELIVER IT TO AN AVERAGE USER VIA THE NEXT GENERATION BUSINESS INTELLIGENCE SOFTWARE WITH RICH INTERNET APPLICATIONS PROVIDES DEVELOPERS DESIGNERS AND ARCHITECTS A SOLID FOUNDATION OF BI DESIGN AND ARCHITECTURE CONCEPTS WITH MICROSOFT SILVERLIGHT THIS BOOK COVERS KEY BI DESIGN CONCEPTS AND HOW THEY CAN BE APPLIED WITHOUT REQUIRING AN EXISTING BI INFRASTRUCTURE THE AUTHOR BART CZERNICKI WILL SHOW YOU HOW TO BUILD SMALL BI APPLICATIONS BY EXAMPLE THAT ARE INTERACTIVE HIGHLY VISUAL STATISTICAL PREDICTIVE AND MOST IMPORTANTLY INTUITIVE TO THE USER BI ISN T JUST FOR THE EXECUTIVE BRANCH OF A FORTUNE 500 COMPANY IT IS FOR THE MASSES LET NEXT GENERATION BUSINESS INTELLIGENCE SOFTWARE WITH RICH INTERNET APPLICATIONS SHOW YOU HOW TO UNLOCK THE RICH INTELLIGENCE YOU ALREADY HAVE

Reconfigurable Computing: Architectures, Tools and Applications 2012-03-22

this book constitutes the refereed proceedings of the 8th international symposium on reconfigurable computing architectures tools and applications arc 2012 held in hongkong china in march 2012 the 35 revised papers presented consisting of 25 full papers and 10 poster papers were carefully reviewed and selected from 44 submissions the topics covered are applied rc design methods and tools applied rc architectures applied rc applications and critical issues in applied rc

Architecture of Computing Systems 2023-09-26

t his book constitutes the proceedings of the 36th international conference on architecture of computing systems arcs 2023 which took place in athens greece in june 2023 the 18 full papers in this volume were carefully reviewed and selected from 35 submissions arcs provides a platform covering newly emerging and cross cutting topics such as autonomous and ubiquitous systems reconfigurable computing and acceleration neural networks and artificial intelligence the selected papers cover a variety of topics from the arcs core domains including energy efficiency applied machine learning hardware and software system security reliable and fault tolerant systems and organic computing back to top

Euro-Par 2008 Workshops - Parallel Processing 2009-04-09

PARALLEL AND DISTRIBUTED PROCESSING ALTHOUGH WITHIN THE FOCUS OF COMPUTER SCIENCE RESEARCH FOR A LONG TIME IS GAINING MORE AND MORE IMPORTANCE IN A WIDE SPECTRUM OF APPLICATIONS THESE PROCEEDINGS AIM TO DEMONSTRATE THE USE OF PARALLEL AND DISTRIBUTED PROCESSING CONCEPTS IN DIFFERENT APPLICATION FIELDS AND

ATTEMPT TO SPARK INTEREST IN NOVEL RESEARCH DIRECTIONS TO PARALLEL AND HIGH PERFORMANCE COMPUTING RESEARCH IN GENERAL THE OBJECTIVE OF THESE WORKSHOPS IS TO SPECIFICALLY ADDRESS RESEARCHERS COMING FROM UNIVERSITY INDUSTRY AND GOVERNMENTAL RESEARCH ORGANIZATIONS AND APPLICATION ORIENTED COMPANIES IN ORDER TO CLOSE THE GAP BETWEEN PURELY SCIENTIFIC RESEARCH AND THE APPLICAB ITY OF THE RESEARCH IDEAS TO REAL LIFE PROBLEMS EURO PAR IS AN ANNUAL SERIES OF INTERNATIONAL CONFERENCES DEDICATED TO THE PRO TION AND ADVANCEMENT OF ALL ASPECTS OF PARALLEL AND DISTRIBUTED COMPUTING THE 2008 EVENT WAS THE 14TH ISSUE OF THE CONFERENCE EURO PAR HAS FOR A LONG TIME BEEN EAGER TO ATTRACT COLOCATED EVENTS SHARING THE SAME GOAL OF PROMOTING THE DEV OPMENT OF PARALLEL AND DISTRIBUTED COMPUTING BOTH AS AN INDUSTRIAL TECHNIQUE AND AN ACADEMIC DISCIPLINE EXTENDING THE FRONTIER OF BOTH THE STATE OF THE ART AND THE STATE OF THE PRACTICE SINCE 2006 EURO PAR HAS BEEN OFFERING RESEARCHERS THE CHANCE TO CO CATE ADVANCED TECHNICAL WORKSHOPS BACK TO BACK WITH THE MAIN CONFERENCE

Introducing .NET 4.0 *2011-01-27*

MICROSOFT HAS INTRODUCED A LARGE NUMBER OF CHANGES TO THE WAY THAT THE NET FRAMEWORK OPERATES FAMILIAR TECHNOLOGIES HAVE BEING ALTERED BEST PRACTICES REPLACED AND DEVELOPER METHODOLOGIES ADJUSTED MANY DEVELOPERS FIND IT HARD TO KEEP UP WITH THE PACE OF CHANGE ACROSS NET S EVER WIDENING ARRAY OF TECHNOLOGIES YOU MAY KNOW WHAT S HAPPENING IN C BUT HOW ABOUT THE AZURE CLOUD HOW IS THAT GOING TO AFFECT YOUR WORK WHAT ARE THE LIMITATIONS OF THE PLINQ SYNTAX WHAT YOU NEED IS A ROADMAP A GUIDE TO HELP YOU SEE THE INNOVATIONS THAT MATTER AND TO GIVE YOU A HEAD START ON THE OPPORTUNITIES AVAILABLE IN THE NEW FRAMEWORK INTRODUCING NET 4 0 WITH VISUAL STUDIO 2010 IS DESIGNED TO PROVIDE YOU WITH JUST THAT ROADMAP IT SERVES AS A NO NONSENSE PRIMER THAT WILL HELP EXPERIENCED NET DEVELOPERS UNDERSTAND THE IMPACT OF THE NEW FRAMEWORK AND ITS ASSOCIATED TECHNOLOGIES THIS BOOK WILL KEEP YOU UPDATED ON THE CHANGES AND HELP YOU TO SEIZE NEW OPPORTUNITIES CONFIDENTLY AND QUICKLY

Euro-Par 2014: Parallel Processing *2014-08-11*

THIS BOOK CONSTITUTES THE REFEREED PROCEEDINGS OF THE 20TH INTERNATIONAL CONFERENCE ON PARALLEL AND DISTRIBUTED COMPUTING EURO PAR 2014 HELD IN PORTO PORTUGAL IN AUGUST 2014 THE 68 REVISED FULL PAPERS PRESENTED WERE CAREFULLY REVIEWED AND SELECTED FROM 267 SUBMISSIONS THE PAPERS ARE ORGANIZED IN 15 TOPICAL SECTIONS SUPPORT TOOLS ENVIRONMENTS PERFORMANCE PREDICTION AND EVALUATION SCHEDULING AND LOAD BALANCING HIGH PERFORMANCE ARCHITECTURES AND COMPILERS PARALLEL AND DISTRIBUTED DATA MANAGEMENT GRID CLUSTER AND CLOUD COMPUTING GREEN HIGH PERFORMANCE COMPUTING DISTRIBUTED SYSTEMS AND ALGORITHMS PARALLEL AND DISTRIBUTED PROGRAMMING PARALLEL NUMERICAL ALGORITHMS MULTICORE AND MANYCORE PROGRAMMING THEORY AND ALGORITHMS FOR PARALLEL COMPUTATION HIGH PERFORMANCE NETWORKS AND COMMUNICATION HIGH PERFORMANCE AND SCIENTIFIC APPLICATIONS AND GPU AND ACCELERATOR COMPUTING

The Art of Multiprocessor Programming *2009-09*

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System Design for Telecommunication Gateways *2011-06-20*

SYSTEM DESIGN FOR TELECOMMUNICATION GATEWAYS PROVIDES A THOROUGH REVIEW OF DESIGNING TELECOMMUNICATION NETWORK EQUIPMENT BASED ON THE LATEST HARDWARE DESIGNS AND SOFTWARE METHODS AVAILABLE ON THE MARKET FOCUSING ON HIGH END EFFICIENT DESIGNS THAT CHALLENGE ALL ASPECTS OF THE SYSTEM ARCHITECTURE THIS BOOK HELPS READERS TO UNDERSTAND A BROADER VIEW OF THE SYSTEM DESIGN ANALYZE ALL ITS MOST CRITICAL COMPONENTS AND SELECT THE PARTS THAT BEST FIT A PARTICULAR APPLICATION IN MANY CASES NEW TECHNOLOGY TRENDS POTENTIAL FUTURE DEVELOPMENTS SYSTEM FLEXIBILITY AND CAPABILITY EXTENSIONS ARE OUTLINED IN PREPARATION FOR THE LONGEVITY TYPICAL FOR PRODUCTS IN THE INDUSTRY KEY FEATURES COMBINES SOFTWARE AND HARDWARE ASPECTS OF THE SYSTEM DESIGN DEFINES COMPONENTS AND SERVICES SUPPORTED BY OPEN SOURCE AND COMMERCIAL BASIC AND EXTENDED SOFTWARE PLATFORMS INCLUDING OPERATING SYSTEMS MIDDLEWARE SECURITY ROUTING MANAGEMENT LAYER AND MORE FOCUSES ON DISRUPTIVE TECHNOLOGIES PROVIDES GUIDELINES FOR DEVELOPING SOFTWARE ARCHITECTURES BASED ON MULTI THREADED MULTI PROCESS MULTI INSTANCE MULTI CORE MULTI CHIP MULTI BLADE AND MULTI CHASSIS DESIGNS COVERS A NUMBER OF ADVANCED HIGH SPEED INTERCONNECT AND FABRIC INTERFACE TECHNOLOGIES AND THEIR COMMERCIAL IMPLEMENTATIONS PRESENTS DIFFERENT SYSTEM FORM FACTORS FROM COMPACT PIZZA BOX STYLES TO MEDIUM AND LARGE BLADED SYSTEMS INCLUDING IBM BLADECENTER ATCA AND MICROTCA BASED CHASSIS DESCRIBES DIFFERENT MEZZANINE CARDS SUCH AS PMC PRPMC XMC AMC AND OTHERS

Embedded Computer Systems: Architectures, Modeling, and Simulation *2019-08-09*

THIS BOOK CONSTITUTES THE REFEREED PROCEEDINGS OF THE 19TH INTERNATIONAL CONFERENCE ON EMBEDDED COMPUTER SYSTEMS ARCHITECTURES MODELING AND SIMULATION SAMOS 2019 HELD IN PYTHAGORION SAMOS GREECE IN JULY

2019 THE 21 REGULAR PAPERS PRESENTED WERE CAREFULLY REVIEWED AND SELECTED FROM 55 SUBMISSIONS THE PAPERS ARE ORGANIZED IN TOPICAL SECTIONS ON SYSTEM DESIGN SPACE EXPLORATION DEEP LEARNING OPTIMIZATION SYSTEM SECURITY MULTI MANY CORE SCHEDULING SYSTEM ENERGY AND HEAT MANAGEMENT MANY CORE COMMUNICATION AND ELECTRONIC SYSTEM LEVEL DESIGN AND VERIFICATION IN ADDITION THERE ARE 13 PAPERS FROM THREE SPECIAL SESSIONS WHICH WERE ORGANIZED ON TOPICS OF CURRENT INTEREST INSIGHTS FROM NEGATIVE RESULTS MACHINE LEARNING IMPLEMENTATIONS AND EUROPEAN PROJECTS