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FPGA□□□□□□ Design and Modeling of Low Power VLSI Systems Soft Computing Systems Proceedings of International Conference on ICT for Sustainable Development Soft Computing for Hybrid Intelligent Systems Secure System Design and Trustable Computing NANO-CHIPS 2030 Algorithms and Architectures for Parallel Processing FPGAs Introduction to Low-Power Design in VLSIs Application-Specific Arithmetic Green Communications FPGA-BASED Hardware Accelerators Testing of Interposer-Based 2.5D Integrated Circuits MATLAB Smart Trends in Information Technology and Computer Communications Energy Efficient and Reliable Embedded Nanoscale SRAM Design 100 Power Tips for FPGA Designers Patent Interference Practice Handbook Excel□□□ □□□□□□ □□□□□□□□□□□□□ The Law of Chemical and Pharmaceutical Invention Applied Reconfigurable Computing. Architectures, Tools, and Applications Evolutionary Design of Intelligent Systems in Modeling, Simulation and Control Visible Light Communications Directory of Corporate Affiliations ISTFA 2014 Methodologies For The Conception, Design, And Application Of Intelligent Systems - Proceedings Of The 4th International Conference On Soft Computing (In 2 Volumes) Field-Programmable Logic and Applications. From FPGAs to Computing Paradigm Handbook of Research on Discrete Event Simulation Environments: Technologies and Applications Mobile Computing and Sustainable Informatics Proceedings of the International Conference on Computers and Devices for Communication Reconfigurable Computing: Architectures, Tools and Applications Design for Embedded Image Processing on FPGAs Selected Papers of the International Workshop on Smalltalk Technologies Wireless Networks Information Processing and Systems Pattern Recognition and Information Processing Field-Programmable Logic and Applications Proceedings of the 5th International Workshop on Reconfigurable Communication-centric Systems on Chip 2010 - ReCoSoC'10 High Performance Integer Arithmetic Circuit Design on FPGA Rapid System Prototyping with FPGAs

FPGAの原理と構成 *2016-04-25*

金融などのビッグデータ処理やロボットの制御などで注目されるfpgaの原理と応用例がわかる 本書は iotやビッグデータ 機械学習といった大規模データ処理のニーズ増に伴って その省電力性と高速な処理速度から注目を集めているfpgaの基盤技術を解説するものです 近年 半導体技術の進歩に伴って やや中途半端であったfpgaの性能が著しく向上した一方で cpuの処理能力の頭打ちが見えてきました そのため cpuやgpuといった汎用デバイスよりも電力効率がよく 且つ高速にデータを処理することができるようになったfpgaが さまざまな応用分野で再注目されています 読者は本書を読み込むことで fpgaのなんたるかを一通り学ぶことができ 要求されるパフォーマンスへの最適な回路設計を図ることができるようになります また いくつかの応用事例も取り上げているので 商品企画や研究テーマの検討にも役立つ内容になっています このような方におすすめ 電子機器 itメーカの技術者 電気電子情報系の大学学部4年生 大学院生主要目次1章 fpgaを理解するための基本事項 2章 fpgaの概要 3章 fpgaの構成 4章 設計フローとツール 5章 設計技術 6章 ハードウェアアルゴリズム 7章 pld fpgaの応用事例 8章 新しいデバイス アーキテクチャ 索引

Design and Modeling of Low Power VLSI Systems *2016-06-06*

very large scale integration vlsi systems refer to the latest development in computer microchips which are created by integrating hundreds of thousands of transistors into one chip emerging research in this area has the potential to uncover further applications for vsli technologies in addition to system advancements design and modeling of low power vlsi systems analyzes various traditional and modern low power techniques for integrated circuit design in addition to the limiting factors of existing techniques and methods for optimization through a research based discussion of the technicalities involved in the vlsi hardware development process cycle this book is a useful resource for researchers engineers and graduate level students in computer science and engineering

Soft Computing Systems *2018-09-24*

this book ccis 837 constitutes the refereed proceedings of the second international conference on soft computing systems icscs 2018 held in sasthamcotta india in april 2018 the 87 full papers were carefully reviewed and selected from 439 submissions the papers are organized in topical sections on soft computing evolutionary algorithms image processing deep learning artificial intelligence big data analytics data minimg machine learning vlsi cloud computing network communication power electronics green energy

Proceedings of International Conference on ICT for Sustainable Development *2016-02-10*

the two volumes of this book collect high quality peer reviewed research papers presented in the international conference on ict for sustainable development ict4sd 2015 held at ahmedabad india during 3 4 july 2015 the book discusses all areas of information and communication technologies and its applications in field for engineering and management the main focus of the volumes are on applications of ict for infrastructure e governance and contemporary technologies advancements on data mining security computer graphics etc the objective of this international conference is to provide an opportunity for the researchers academicians industry persons and students to interact and exchange ideas experience and expertise in the current trend and strategies for information and communication technologies

Soft Computing for Hybrid Intelligent Systems *2008-08-25*

we describe in this book new methods and applications of hybrid intelligent systems using soft computing techniques soft computing sc consists of several intelligent computing paradigms including fuzzy logic neural networks and evolutionary al rithms which can be used to produce powerful hybrid intelligent systems the book is organized in five main parts which contain a group of papers around a similar subject the first part consists of papers with the main theme of intelligent control which are basically papers that use hybrid systems to solve particular problems of control the second part contains papers with the main theme of pattern recognition which are basically papers using soft computing techniques for achieving pattern recognition in different applications the third part contains papers with the themes of intelligent agents and social systems which are papers that apply the ideas of agents and social behavior to solve real world problems the fourth part contains papers that deal with the hardware implementation of intelligent systems for solving particular problems the fifth part contains papers that deal with modeling simulation and optimization for real world applications

Secure System Design and Trustable Computing *2015-09-17*

this book provides the foundations for understanding hardware security and trust which have become major concerns for national security over the past decade coverage includes issues related to security and trust in a variety of electronic devices and systems related to the security of hardware firmware and software spanning system applications online transactions and networking services this serves as an invaluable reference to the state of the art research that is of critical significance to the security of and trust in modern society s microelectronic supported infrastructures

NANO-CHIPS 2030 2020-06-08

in this book a global team of experts from academia research institutes and industry presents their vision on how new nano chip architectures will enable the performance and energy efficiency needed for ai driven advancements in autonomous mobility healthcare and man machine cooperation recent reviews of the status quo as presented in chips 2020 springer have prompted the need for an urgent reassessment of opportunities in nanoelectronic information technology as such this book explores the foundations of a new era in nanoelectronics that will drive progress in intelligent chip systems for energy efficient information technology on chip deep learning for data analytics and quantum computing given its scope this book provides a timely compendium that hopes to inspire and shape the future of nanoelectronics in the decades to come

Algorithms and Architectures for Parallel Processing *2012-09-04*

the two volume set lncs 7439 and 7440 comprises the proceedings of the 12th international conference on algorithms and architectures for parallel processing ica3pp 2012 as well as some workshop papers of the cdcn 2012 workshop which was held in conjunction with this conference the 40 regular paper and 26 short papers included in these proceedings were carefully

reviewed and selected from 156 submissions the cdcn workshop attracted a total of 19 original submissions 8 of which are included in part ii of these proceedings the papers cover many dimensions of parallel algorithms and architectures encompassing fundamental theoretical approaches practical experimental results and commercial components and systems

FPGAs 2017-07-28

field programmable gate arrays fpgas are currently recognized as the most suitable platform for the implementation of complex digital systems targeting an increasing number of industrial electronics applications they cover a huge variety of application areas such as aerospace food industry art industrial automation automotive biomedicine process control military logistics power electronics chemistry sensor networks robotics ultrasound security and artificial vision this book first presents the basic architectures of the devices to familiarize the reader with the fundamentals of fpgas before identifying and discussing new resources that extend the ability of the devices to solve problems in new application domains design methodologies are discussed and application examples are included for some of these domains e g mechatronics robotics and power systems

Introduction to Low-Power Design in VLSIs *2011-12-12*

this book discusses one increasingly important issue in the vlsi design low power it covers the following topics a basic concepts of low power design b low power design methods and applications in industry chips and c commercial cad tools on low power design this book discusses the concepts a set of known methods industry cases and cad tools on the low power design it is organized in four chapters and a glossary is provided at the end of the book

Application-Specific Arithmetic 2016-04-19

nowadays energy crisis and global warming problems are hanging over everyone s head urging much research work on energy saving in the ict industry which is becoming a major consumer of global energy triggered by the telecommunication network operators experiencing energy cost as a significant factor in profit calculations researchers have start

Green Communications 2019-05-30

this book suggests and describes a number of fast parallel circuits for data vector processing using fpga based hardware accelerators three primary areas are covered searching sorting and counting in combinational and iterative networks these include the application of traditional structures that rely on comparators swappers as well as alternative networks with a variety of core elements such as adders logical gates and look up tables the iterative technique discussed in the book enables the sequential reuse of relatively large combinational blocks that execute many parallel operations with small propagation delays for each type of network discussed the main focus is on the step by step development of the architectures proposed from initial concepts to synthesizable hardware description language specifications each type of network is taken through several stages including modeling the desired functionality in software the retrieval and automatic conversion of key functions leading to specifications for optimized hardware modules the resulting specifications are then synthesized implemented and tested in fpgas using commercial design environments and prototyping boards the methods proposed can be used in a range of data processing applications including traditional sorting the extraction of maximum and minimum subsets from large data sets communication time data processing finding frequently occurring items in a set and hamming weight distance counters comparators the book is intended to be a valuable support material for university and industrial engineering courses that involve fpga based circuit and system design

FPGA-BASED Hardware Accelerators *2017-03-20*

this book provides readers with an insightful guide to the design testing and optimization of 2 5d integrated circuits the authors describe a set of design for test methods to address various challenges posed by the new generation of 2 5d ics including pre bond testing of the silicon interposer at speed interconnect testing built in self test architecture extest scheduling and a programmable method for low power scan shift in soc dies this book covers many testing techniques that have already been used in mainstream semiconductor companies readers will benefit from an in depth look at test technology solutions that are needed to make 2 5d ics a reality and commercially viable

Testing of Interposer-Based 2.5D Integrated Circuits *2012-09-26*

this excellent book represents the second part of three volumes regarding matlab based applications in almost every branch of science the present textbook contains a collection of 13 exceptional articles in particular the book consists of three sections the first one is devoted to electronic engineering and computer science the second is devoted to matlab simulink as a tool for engineering applications the third one is about telecommunication and communication systems and the last one discusses matlab toolboxes

MATLAB *2018-08-20*

this book constitutes the refereed proceedings of the second international conference on smart trends in information technology and computer communications smartcom 2017 held in pune india in august 2017 the 38 revised papers presented were carefully reviewed and selected from 310 submissions the papers address issues on smart and secure systems smart and service computing smart data and it innovations

Smart Trends in Information Technology and Computer Communications *2023-11-29*

this reference text covers a wide spectrum for designing robust embedded memory and peripheral circuitry it will serve as a useful text for senior undergraduate and graduate students and professionals in areas including electronics and communications engineering electrical engineering mechanical engineering and aerospace engineering discusses low power design

methodologies for static random access memory sram covers radiation hardened sram design for aerospace applications focuses on various reliability issues that are faced by submicron technologies exhibits more stable memory topologies nanoscale technologies unveiled significant challenges to the design of energy efficient and reliable srams this reference text investigates the impact of process variation leakage aging soft errors and related reliability issues in embedded memory and periphery circuitry the text adopts a unique way to explain the sram bitcell array design and analysis of its design parameters to meet the sub nano regime challenges for complementary metal oxide semiconductor devices it comprehensively covers low power design methodologies for sram exhibits more stable memory topologies and radiation hardened sram design for aerospace applications every chapter includes a glossary highlights a question bank and problems the text will serve as a useful text for senior undergraduate students graduate students and professionals in areas including electronics and communications engineering electrical engineering mechanical engineering and aerospace engineering discussing comprehensive studies of variability induced failure mechanism in sense amplifiers and power delay and read yield trade offs this reference text will serve as a useful text for senior undergraduate graduate students and professionals in areas including electronics and communications engineering electrical engineering mechanical engineering and aerospace engineering it covers the development of robust srams well suited for low power multi core processors for wireless sensors node battery operated portable devices personal health care assistants and smart internet of things applications

Energy Efficient and Reliable Embedded Nanoscale SRAM Design *1998-01-01*

one procedural misstep in patent interference practice can put an invention at risk patent interference practice handbook is the only book that leads you step by step through proper procedure at every stage of the interference process before and after declaration covering practice before the u s patent office the district courts and the court of appeals for the federal circuit this intensely practical guide shows you exactly how to assess elements such as anticipation use or sale obviousness abandonment suppression concealmentestablish patentabilitydetermine prioritymeet reduction to practice standardsmeet all burden of proof requirementsavoid export license violationsfile preliminary statements and motionsbring civil actions or appeals after interference at every stage of his p

100 Power Tips for FPGA Designers *2015-12-18*

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Patent Interference Practice Handbook 2012

this book constitutes the proceedings of the 14th international conference on applied reconfigurable computing arc 2018 held in santorini greece in may 2018 the 29 full papers and 22 short presented in this volume were carefully reviewed and selected from 78 submissions in addition the volume contains 9 contributions from research projects the papers were organized in topical sections named machine learning and neural networks fpga based design and cgra optimizations applications and surveys fault tolerance security and communication architectures reconfigurable and adaptive architectures design methods and fast prototyping fpga based design and applications and special session research projects

Excel[illegible] [illegible] *2018-04-25*

we describe in this book new methods for evolutionary design of intelligent s tems using soft computing and their applications in modeling simulation and c trol soft computing sc consists of several intelligent computing paradigms including fuzzy logic neural networks and evolutionary algorithms which can be used to produce powerful hybrid intelligent systems the book is organized in four main parts which contain a group of papers around a similar subject the first part consists of papers with the main theme of evolutionary design of fuzzy systems in intelligent control which consists of papers that propose new methods for designing and optimizing intelligent controllers for different applications the second part c tains papers with the main theme of evolutionary design of intelligent systems for pattern recognition applications which are basically papers using evolutionary al rithms for optimizing modular neural networks with fuzzy systems for response tegration for achieving pattern recognition in different applications the third part contains papers with the themes of models for learning and social simulation which are papers that apply intelligent systems to the problems of designing learning jects and social agents the fourth part contains papers that deal with intelligent s tems in robotics applications and hardware implementations in the part of intelligent control there are 5 papers that describe different c tributions on evolutionary optimization of fuzzy systems in intelligent control the first paper by ricardo martinez marroquin et al

The Law of Chemical and Pharmaceutical Invention *2009-10-13*

visible light communications written by leading researchers provides a comprehensive overview of theory stimulation design implementation and applications the book is divided into two parts the first devoted to the underlying theoretical concepts of the vlc and the second part covers vlc applications visible light communications is an emerging topic with multiple functionalities including data communication indoor localization 5g wireless communication networks security and small cell optimization this concise book will be of valuable interest from beginners to researchers in the field

Applied Reconfigurable Computing. Architectures, Tools, and Applications 2017-06-26

this volume features the latest research and practical data from the premier event for the microelectronics failure analysis community the papers address the symposium s theme exploring the many facets of failure analysis

Evolutionary Design of Intelligent Systems in Modeling, Simulation and Control *2003*

iizuka 96 the 4th international conference on soft computing emphasized the integration of the components of soft computing to promote the research work on post digital computers and to realize the intelligent systems at the conference new developments and results in soft computing were introduced and discussed by researchers from academic governmental and industrial institutions this volume presents the opening lectures by prof lotfi a zadeh and prof walter j freeman the plenary lectures by seven eminent researchers and about 200 carefully selected papers drawn from more than 20 countries it documents current research and in depth studies on the conception design and application of intelligent systems

Visible Light Communications *2014-11-01*

this book constitutes the refereed proceedings of the 8th international workshop on field programmable logics and applications fpl 98 held in tallinn estonia in august september 1998 the 39 revised full papers presented were carefully selected for inclusion in the book from a total of 86 submissions also included are 30 refereed high quality posters the papers are organized in topical sections on design methods general aspects prototyping and simulation development methods accelerators system architectures hardware software codesign system development algorithms on fpgas and applications

Directory of Corporate Affiliations *1996-08-31*

this book provides a comprehensive overview of theory and practice in simulation systems focusing on major breakthroughs within the technological arena with particular concentration on the accelerating principles concepts and applications provided by publisher

ISTFA 2014 *1998-08-14*

this book gathers selected high quality research papers presented at international conference on mobile computing and sustainable informatics icmcsi 2022 organized by pulchowk campus institute of engineering tribhuvan university nepal during 27 28 january 2022 the book discusses recent developments in mobile communication technologies ranging from mobile edge computing devices to personalized embedded and sustainable applications the book covers vital topics like mobile networks computing models algorithms sustainable models and advanced informatics that supports the symbiosis of mobile computing and sustainable informatics

Methodologies For The Conception, Design, And Application Of Intelligent Systems - Proceedings Of The 4th International Conference On Soft Computing (In 2 Volumes) *2009-10-31*

this book constitutes the refereed proceedings of the third international workshop on applied reconfigurable computing arc 2007 held in mangaratiba brazil in march 2007 the 27 full papers and 10 short papers presented together with a late comer contribution from arc 2006 are organized in topical sections on architectures mapping techniques and tools arithmetic and applications

Field-Programmable Logic and Applications. From FPGAs to Computing Paradigm *2022-07-15*

design for embedded image processing on fpgas bridge the gap between software and hardware with this foundational design reference field programmable gate arrays fpgas are integrated circuits designed so that configuration can take place circuits of this kind play an integral role in processing images with fpgas increasingly embedded in digital cameras and other devices that produce visual data outputs for subsequent realization and compression these uses of fpgas require specific design processes designed to mediate smoothly between hardware and processing algorithm design for embedded image processing on fpgas provides a comprehensive overview of these processes and their applications in embedded image processing beginning with an overview of image processing and its core principles this book discusses specific design and computation techniques with a smooth progression from the foundations of the field to its advanced principles readers of the second edition of design for embedded image processing on fpgas will also find detailed discussion of image processing techniques including point operations histogram operations linear transformations and more new chapters covering deep learning algorithms and image and video coding example applications throughout to ground principles and demonstrate techniques design for embedded image processing on fpgas is ideal for engineers and academics working in the field of image processing as well as graduate students studying embedded systems engineering image processing digital design and related fields

Handbook of Research on Discrete Event Simulation Environments: Technologies and Applications *1998*

the goal of the iwst workshop series is to create and foster a forum around advancements of or experience in smalltalk the workshop welcomes contributions to all aspects theoretical as well as practical of smalltalk related topics

Mobile Computing and Sustainable Informatics *2007-06-04*

the international multi topic conference imtic 2008 was held in pakistan during april 11 12 2008 it was a joint venture between mehran university jamshoro sindh and aalborg university esbjerg denmark apart from the two day main event two workshops were also held the workshop on creating social semantic 2 0 information spaces and the workshop on wireless sensor networks two hundred participants registered for the main conference from 24 countries and 43 papers were presented the two workshops had overwhelming support and over 400 delegates registered imtic 2008 served as a platform for international

scientists and the engineering community in general and in particular for local scientists and the engineering c munity to share and cooperate in various fields of interest the topics presented had a reasonable balance between theory and practice in multidisciplinary topics the c ference also had excellent topics covered by the keynote speeches keeping in view the local requirements which served as a stimulus for students as well as experienced participants the program committee and various other committees were experts in their areas and each paper went through a double blind peer review process the c ference received 135 submissions of which only 46 papers were selected for presen tion an acceptance rate of 34

Proceedings of the International Conference on Computers and Devices for Communication 2023-08-08

this book constitutes the refereed proceedings of the 14th international conference on pattern recognition and information processing prip 2019 held in minsk belarus in may 2019 the 25 revised full papers were carefully reviewed and selected from 120 submissions the papers of this volume are organized in topical sections on pattern recognition and image analysis information processing and applications

Reconfigurable Computing: Architectures, Tools and Applications *2010*

this book constitutes the refereed proceedings of the 13th international conference on field programmable logic and applications fpl 2003 held in lisbon portugal in september 2003 the 90 revised full papers and 56 revised poster papers presented were carefully reviewed and selected from 216 submissions the papers are organized in topical sections on technologies and trends communications applications high level design tools reconfigurable architecture cryptographic applications multi context fpgas low power issues run time reconfiguration compilation tools asynchronous techniques bio related applications codesign reconfigurable fabrics image processing applications sat techniques application specific architectures dsp applications dynamic reconfiguration soc architectures emulation cache design arithmetic bio inspired design soc design cellular applications fault analysis and network applications

Design for Embedded Image Processing on FPGAs *2008-11-14*

this book describes the optimized implementations of several arithmetic datapath controlpath and pseudorandom sequence generator circuits for realization of high performance arithmetic circuits targeted towards a specific family of the high end field programmable gate arrays fpgas it explores regular modular cascadable and bit sliced architectures of these circuits by directly instantiating the target fpga specific primitives in the hdl every proposed architecture is justified with detailed mathematical analyses simultaneously constrained placement of the circuit building blocks is performed by placing the logically related hardware primitives in close proximity to one another by supplying relevant placement constraints in the xilinx proprietary user constraints file the book covers the implementation of a gui based cad tool named flexicore integrated with the xilinx integrated software environment ise for design automation of platform specific high performance arithmetic circuits from user level specifications this tool has been used to implement the proposed circuits as well as hardware implementations of integer arithmetic algorithms where several of the proposed circuits are used as building blocks implementation results demonstrate higher performance and superior operand width scalability for the proposed circuits with respect to implementations derived through other existing approaches this book will prove useful to researchers students and professionals engaged in the domain of fpga circuit optimization and implementation

Selected Papers of the International Workshop on Smalltalk Technologies 2019-11-22

the push to move products to market as quickly and cheaply as possible is fiercer than ever and accordingly engineers are always looking for new ways to provide their companies with the edge over the competition field programmable gate arrays fpgas which are faster denser and more cost effective than traditional programmable logic devices plds are quickly becoming one of the most widespread tools that embedded engineers can utilize in order to gain that needed edge fpgas are especially popular for prototyping designs due to their superior speed and efficiency this book hones in on that rapid prototyping aspect of fpga use showing designers exactly how they can cut time off production cycles and save their companies money drained by costly mistakes via prototyping designs with fpgas first reading it will take a designer with a basic knowledge of implementing fpgas to the next level of fpga use because unlike broad beginner books on fpgas this book presents the required design skills in a focused practical example oriented manner in the trenches expert authors assure the most applicable advice to practicing engineers dual focus on successfully making critical decisions and avoiding common pitfalls appeals to engineers pressured for speed and perfection hardware and software are both covered in order to address the growing trend toward cross pollination of engineering expertise

Wireless Networks Information Processing and Systems *2003-08-27*

Pattern Recognition and Information Processing *2010*

Field-Programmable Logic and Applications *2015-07-06*

Proceedings of the 5th International Workshop on Reconfigurable Communication-centric Systems on Chip 2010 - ReCoSoC'10 *2011-03-31*

High Performance Integer Arithmetic Circuit Design on FPGA

Rapid System Prototyping with FPGAs

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